第IV部

附属書

ここでは、High Performance Fortran 言語 バージョン 2.0 (第 I 部と第 II 部で記述) および、公認拡張仕様 (第 III 部で記述) の機能の文法と意味について記述を、参照利用の ためにまとめる。ここは、厳密な意味での HPF 言語仕様書の一部ではない。

附属書A 構文規則

この附属書は、本 High Performance Fortran 言語仕様書で定義される構文規則の一覧である。

A.2 記述法と構文

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A.2.2 指示文の構文

H201	hpf- $directive$ - $line$	is	directive-orig	gin hpf-directive

H202	$directive \hbox{-} origin$	is	!HPF\$
		or	CHPF\$

or *HPF\$

H203 hpf-directive is specification-directive

 ${\bf or} \quad executable\text{-}directive$

H204 specification-directive is processors-directive

or align-directive
or distribute-directive
or inherit-directive
or template-directive

or combined-directive or sequence-directive

H205 executable-directive is independent-directive

制約: hpf-directive-line には、他の文があってはならない。

制約: specification-directive は、宣言構文 (declaration-construct) が現れてもよい場所にのみ 現れてもよい。

制約: executable-directive は、実行構文 (executable-construct) が現れてもよい場所にのみ現れてもよい。

制約: hpf-directive-line は、プログラム単位内の周囲の形式に従って、Fortran の自由形式 (F95:3.3.1.1) または固定形式 (F95:3.3.2.1) の注釈行の規則のどちらかに従う。(F95:3.3)

H206	$specification\mbox{-}directive\mbox{-}extended$	is	$processors ext{-}directive$	1
	1 "	or	subset-directive	2
		or	$align\mbox{-}directive$	3
		or	distribute- $directive$	4
		or	inherit-directive	5
		or	$template \hbox{-} directive$	6
		or	combined-directive	7 8
		or	sequence-directive	9
		or	dynamic- $directive$	10
		or	range-directive	11
		or	shadow- $directive$	12
				13
H207	$executable ext{-}directive ext{-}extended$	ia	independent directive	14 15
11207	executavie-an ective-extended	is	independent-directive $realign$ -directive	16
		or	redistribute-directive	17
		or or	on-directive	18
		or	resident-directive	19
		OI	Testuetti-utteetive	20
H208	$executable\hbox{-} construct\hbox{-} extended$	is	action-stmt	21
		or	case-construct	22 23
		or	do-construct	24
		\mathbf{or}	$if ext{-}construct$	25
		or	$where \hbox{-} construct$	26
		or	$on ext{-}construct$	27
		or	$resident ext{-}construct$	28
		or	task-region-construct	29
				30 31
A.3	データマッピング			32
_			- 144 3	33
A.3.2	2 データ整列とデータ分散指	示文	ての構文	34
H301	combined- $directive$	is	combined-attribute-list $:: combined$ -decl-list	35
11200	line of other last.	•_	AT TON Alian Alleria de 166	36
H302	combined- $attribute$	is	ALIGN align-attribute-stuff	37
		or	DISTRIBUTE dist-attribute-stuff	38 39
		or	INHERIT	40
		or	TEMPLATE	41
		or	PROCESSORS	42
		or	DIMENSION ($explicit\text{-}shape\text{-}spec\text{-}list$)	43
H303	combined-decl	is	$hpf entity \ [\ (\ explicit entire shape entire spec entire list \) \]$	44
		\mathbf{or}	$object{-}name$	45
				46

H304 hpf-entity 1 is processors-name2 **or** template-name 3 制約: 同じ種類の combined-attribute を、一つの combined-directive 中で 2 回以上指定しては 4 5 ならない。 6 制約: DIMENSION 属性が combined-directive に現れた場合、それが適用されるすべての言語 7 8 要素は、HPFの TEMPLATEや PROCESSORS型指定子で宣言されなけばならない。 9 10 A.3.3 DISTRIBUTE 指示文 11 12 H305 distribute-directive DISTRIBUTE distributee dist-directive-stuff is13 dist-format-clause [dist-onto-clause] H306 dist-directive-stuff is 14 15H307 dist-attribute-stuff dist-directive-stuff is 16 ${f or}~~dist$ -onto-clause 17 18 H308 distribute e object-name is 19 **or** template-name 20 H309 dist-format-clause (dist-format-list)is 21 or * (dist-format-list) 22 23 or * 24 is BLOCK [(scalar-int-expr)] H310 dist-format 25 or CYCLIC [(scalar-int-expr)] 26 27 or * 28 H311 dist-onto-clause isONTO dist-target 29 30 H312 dist-target processors-name is31 or * processors-name 32 or * 33 34 制約: distributee である実体名 (object-name) は、単純な名前でなければならず、部分実体特 35 定子や成分名 (component-name) であってはならない。 36 37 制約: distributee である実体名 (object-name) は、alignee であってはならない。 38 39 制約: distributee である実体名 (object-name) は、POINTER 属性をもつことはできない。 40 制約: distributeeである実体名(object-name)は、TARGET属性をもつことはできない。 4142 制約: distributee がスカラであるとき、dist-format-list(およびそれを囲む丸括弧) は現れては 43 ならない。この場合、文形式の指示文は、dist-format-clause が "*" である場合だけが 44 許される。 45 46 47

制約: dist-format-list が指定されたら、その長さはそれぞれの distributee の次元数と等しくなければならない。

制約:		両方が指定されたとき、" st "でない $dist$ - $format$ - $list$ の $_1$ 成の次元数と等しくなければならない。						
制約:	$dist ext{-}format ext{-}list$ がなく $dist ext{-}target$ が指定されたとき、それぞれの $distributee$ の次元数 3 は、そのプロセッサ構成の次元数と等しくなければならない。 5							
制約:	DISTRIBUTE 指示文で、 $dist$ -forるときは、すべての $distributee$	$mat ext{-}clause$ または $dist ext{-}target$ のどちらかが " st " で始ま eta 7 は仮引数でなければならない。 st 8						
制約:	DISTRIBUTE 指示文の dist-form (specification-expr) でなくては	mat 中の整数式 (scalar-int-expr) は、すべて宣言式 10ならない。						
A.3.	4 ALIGN 指示文	12 13						
H313	$align ext{-}directive$	is ALIGN alignee align-directive-stuff						
H314	align-directive-stuff	is (align-source-list) align-with-clause						
		17						
H315	$align ext{-}attribute ext{-}stuff$	is [(align-source-list)] align-with-clause 18						
H316	alignee	is object-name						
H317	align-source	is : 21						
		or * or align-dummy						
TT010		24						
H318	$align ext{-}dummy$	is scalar-int-variable 25						
制約:	alignee である実体名 (object-na 子や成分名 (component-name)	$me)$ は、単純な名前でなければならず、部分実体特定 27 であってはならない。 28						
制約:	alignee である実体名 (object-na	me) は、 $distributee$ であってはならない。						
制約:	alignee である実体名 (object-na	me) は、POINTER 属性をもつことはできない。 31						
制約:	alignee である実体名 (object-na	$me)$ は、TARGET 属性をもつことはできない。 33						
制約:	alignee がスカラであるとき、 a ならない。この場合、文形式の	lign-source-list (およびそれを囲む丸括弧) は現れては 35 指示文は許されない。						
制約:	align-source-listが指定されたとらない。	37 こき、その長さは alignee の次元数と等しくなければな 38 39						
制約:	align-dummy は、名前付き変数							
制約:	宝休け INHERIT 屋性と ハエエウ	M 属性の両方をもつことはできない。 43						
ניאניטו.		N 属圧の同力をもうことはくこない。 44						
H319	$a lign\hbox{-} with\hbox{-} clause$	is WITH align-spec						
H320	align- $spec$	is $align\text{-}target$ [($align\text{-}subscript\text{-}list$)]						
		$\mathbf{or} * align\text{-}target [(align\text{-}subscript\text{-}list)] $						

1	H321	$align ext{-}target$	is	$object{-}name$		
2			or	template- $name$		
3	H322	align-subscript	is	int- $expr$		
5			or	$align\hbox{-}subscript\hbox{-}use$		
6			or	subscript-triplet		
7				*		
8			01			
9 10	H323	$align\hbox{-}subscript\hbox{-}use$	is	$[\ [\ int\text{-}level\text{-}two\text{-}expr \] \ add\text{-}op \] \ align\text{-}add\text{-}operand$		
11			\mathbf{or}	align-subscript-use add -op int -add-operand		
12	TT00.4					
13	H324	$align\hbox{-} add\hbox{-} oper and$	is	[int-add-operand *] align-primary		
14			\mathbf{or}	align-add-operand * int-mult-operand		
15	H325	align-primary	is	align- $dummy$		
16	11020	awgre premeary		(align-subscript-use)		
17			or	(anyn-suoscript-use)		
18 19	H326	int- add - $oper$ and	is	$add ext{-}operand$		
20	H327	int- $mult$ - $operand$	is	mult-operand		
21 22	H328	int-level-two-expr	is	$level ext{-}2 ext{-}expr$		
23 24	制約:	align-target である実体名 $(object-name)$ は、単純な名前でなければならず、部分実体特定子や成分名 $(component-name)$ であってはならない。				
25 26	制約:	align-targetは、 OPTIONAL 属性をもつことはできない。				
27	制約:	ALIGN 指示文の alian-spec が"*	." で	始まるときは、すべての alignee は仮引数でなけ		
28	. C.W.Cdi	ればならない。				
29		10000000				
30	制約:	align-directive 中の整数式 (int-e:	xpr),	、int-level-two-expr、int-add-operand および int-		
31		mult-operandは、宣言式でなく ⁻	- /			
32		<u>-</u>				
33	制約:	align-directive 中の align-subscr	ipt 7	である添字三つ組 (subscript-triplet) の添字 (sub-		
34		script) や刻み幅 $(stride)$ は、宣言	言式で	でなくてはならない。		
35						
36	制約:	align-subscript-listでは、同一の	alig	n-dummy は高々 1 回だけ現れることができる。		
37	生山石	1: 1 : 1 	7	けうち 同だけ扱わって とができっ		
38	制約:	augn-suoscript-use Evicia, augr	n-au	mmyは高々 1 回だけ現れることができる。		
39 40	制約:	alian-dummu として使われた sc	alar-	int-variable は、上記の文法により明確に許され		
41		9		現れてはならない。言い代えれば、一つの align-		
42						
43		• •		N整数宣言式を加数や乗数として加えることでの		
44		み、align-subscript-use を構成す	ာ င	. こか じざる。		
45	制約:	alian-subscrint中の添字 (subscri	nt) 1;	には、いかなる align-dummy も現れてはならない。		
46	. r.w.c.d.	Land of the Land of the Company	(° ')			

制約: int-add-operand, int-mult-operand および int-level-two-expr は、整数型でなくてはな

47

らない。

A.3.6 PROCESSORS 指示文			1
H329 processors-directive	is	${\tt PROCESSORS} \ \ processors\text{-}decl\text{-}list$	2
H330 processors-decl	is	processors-name	4
P		[(explicit-shape-spec-list)]	5
			6
A.3.7 TEMPLATE 指示文			7
		TOWN ATTO A LABOR LABOR.	8 9
H331 template-directive	is	${\tt TEMPLATE} \ \ template\text{-} decl\text{-} list$	10
H332 $template-decl$	is	$template{-name} \ [\ (\ explicit{-shape-spec-list} \) \]$	11
			12
			13
A.3.8 記憶列結合と順序結合			14 15
H333 sequence-directive	is	SEQUENCE [[::] association-name-list]	16
11000 sequence un contro	or	NO SEQUENCE [::] association-name-list]	17
11994	_		18 19
H334 association-name	is	object-name	20
	or	/ [common-block-name] /	21
制約: いかなる有効域においても、デー	- 夕 写	E体の名前と共通ブロック名は、sequence-directive	22
の中に高々一度しか現れてはな		-	23
Maril Ada			24 25
·	quen	ce-directive は、同じ有効域の中で一つしか許され	26
ない。			27
		. 1 . 2 4.5	28
A.4 副プログラム境界でのデータ	/ / <i>/</i>	DE 20	29
A.4.4 整列			30 31
H401 inherit-directive	is	INHERIT inheritee-list	32
			33
H402 inheritee	is	$object{-}name$	34 35
制約: inheritee は仮引数でなければな	らな	L).	36
ipania. State of the light of the state of t	J 0.	• •	37
制約: inheritee は alignee であってはな	よらな	III.	38
制約: inheritee は distributee であって	はな	らない。	39 40
			41
A.5 INDEPENDENT指示文及	57ド目	関連の指示文	42
			43
A.5.1 INDEPENDENT 指示文	7		44
H501 independent-directive	is	INDEPENDENT $[$, new - $clause$ $]$	45 46
22001 monoponuncino uni como	10	[, reduction-clause]	47
		[,	48

1	H502	new-clause	is	NEW (variable-name-list)
2	H503	$reduction\hbox{-} clause$	is	REDUCTION (reduction-variable-list)
3 4 5 6 7	H504	$reduction\hbox{-} variable$	is or or	$array ext{-}variable ext{-}name$ $scalar ext{-}variable ext{-}name$ $structure ext{-}component$
8 9 10	制約:	independent-directive の後の最初constructでなければならない。]の∄	非注釈行は、do-stmt、forall-stmt、または forall-
11 12 13	制約:	independent-directive の後の最初 able を含む loop-control オプショ		‡注釈行が $do ext{-}stmt$ である場合、その文は $do ext{-}vari ext{-}$ を持たなければならない。
14 15 16	制約:	NEW 節または REDUCTION 節があっければならない。	る場	合、指示文の後の最初の非注釈行は $\emph{do-stmt}$ でな
17 18	制約:	NEW 節または REDUCTION 節内に打 ものであってはならない。	指定	する variable 及びそれらの成分及び要素は、次の
20 21 22 23 24 25 26 27 28 29		 仮引数 SAVE 属性または TARGET 属 COMMON プロックに現われる EQUIVALENCE 文により他の 参照結合したもの 親子結合したもの 親子結合により他の有効域 	sも <i>0</i> 実体	と記憶列結合するもの
31 32 33 34 35	制約:	に現われてはならず、independe	nt-d すな:	数は、同じ independent-directive の new-clause 中 lirective が適用される後続の do-stmt、forall-stmt わち、ソース上でのループ本体部) の new-clause ならない。
36 37 38	制約:	reduction-variable 中の structure ない。	-con	nponentは、subscript-section-listを含んではなら
39 40 41 42 43 44	制約:	reduction-variable ¹ として現われ CHARACTER 型であってはならない		変数は、組込み型でなければならない。また、

¹原文は reduction-var

```
H505 reduction-stmt
                                      variable = variable mult-op mult-operand
                                  is
                                                                                  1
                                  or variable = add-operand * variable
                                  or variable = variable add-op add-operand
                                  or variable = level-2-expr + variable
                                     variable = variable and-op and-operand
                                  or variable = and-operand and-op variable
                                  or variable = variable or-op or-operand
                                  or variable = or-operand or-op variable
                                  or variable = variable equiv-op equiv-operand
                                                                                  10
                                                                                  11
                                  or variable = equiv-operand equiv-op variable
                                                                                 )<sup>12</sup>
                                  or variable = reduction-function ( variable , expr
                                     variable = reduction-function ( expr , variable )_{14}^{"}
                                                                                  15
H506 reduction-function
                                  is
                                      MAX
                                                                                  16
                                  or MIN
                                                                                  17
                                  or IAND
                                                                                  18
                                  or IOR
                                                                                  19
                                  or IEOR
                                                                                  20
                                                                                  21
制約: reduction-stmt中に variableが 2 つ現われる場合、その 2 つは字面的に等しくなければ
                                                                                  22
      ならない。
                                                                                  23
                                                                                  ^{24}
                                                                                  25
      外来プログラム単位
                                                                                  26
                                                                                  27
A.6.2
        外来プログラム単位の宣言
                                                                                  28
H601 function-stmt
                                     [ prefix ] FUNCTION function-name
                                                                                  29
                                        ([dummy-arg-name-list])
                                                                                  30
                                                                                  31
                                        [ RESULT ( result-name ) ]
                                                                                  32
H602 subroutine-stmt
                                     [ prefix ] SUBROUTINE subroutine-name
                                                                                  33
                                        [ ( [ dummy-arg-list ] ) ]
                                                                                  34
                                                                                  35
H603 prefix
                                     prefix-spec [ prefix-spec ] \dots
                                  is
                                                                                  36
H604 prefix-spec
                                  is
                                      type-spec
                                                                                  37
                                                                                  38
                                  or RECURSIVE
                                                                                  39
                                  or PURE
                                                                                  40
                                  or ELEMENTAL
                                                                                  41
                                  or extrinsic-prefix
                                                                                  42
                                                                                  43
制約: 任意の HPF の外部副プログラム内で、どの内部副プログラムも、その親と同じ外来種
                                                                                  44
     別でなければならず、また、明示的に外来種別が与えられていない内部副プログラム
                                                                                  45
     に対しては、その親と同じ外来種別が仮定される。
                                                                                  46
                                                                                  47
```

[extrinsic-prefix] PROGRAM program-name

48

H605 program-stmt

```
 [ \ extrinsic\text{-}prefix \ ] \ \texttt{MODULE} \ module\text{-}name \\
    H606 \quad module\text{-}stmt
                                   is
1
2
    H607 block-data-stmt
                                      [ extrinsic-prefix ] BLOCK DATA
                                   is
3
                                         [ block-data-name ]
4
5
    制約: 任意の HPF のモジュール内で、どのモジュール副プログラムも、その親と同じ外来種
6
          別でなければならず、また明示的に外来種別が与えられていないモジュール副プログ
7
          ラムに対しては、その親と同じ外来種別が仮定される。
8
9
    制約: HPFの任意の主プログラムあるいはモジュール副プログラム内で、どの内部副プログ
10
          ラムも、その親と同じ外来種別でなければならず、また明示的に外来種別が与えられ
11
          ていない内部副プログラムに対しては、その親と同じ外来種別が仮定される。
12
13
14
    H608 extrinsic-prefix
                                   is EXTRINSIC ( extrinsic-spec )
15
    H609 extrinsic-spec
                                      extrinsic-spec-arq-list
                                   is
16
                                      extrinsic-kind-keyword
17
18
    H610 extrinsic-spec-arg
                                      language
                                   is
19
                                   or model
20
                                     external-name
                                   \mathbf{or}
21
^{22}
    H611 language
                                      LANGUAGE =
23
                                         scalar\text{-}char\text{-}initialization\text{-}expr
^{24}
    H612 model
                                      [MODEL = ]
25
^{26}
                                         scalar\text{-}char\text{-}initialization\text{-}expr
27
    H613 external-name
                                      [ EXTERNAL_NAME = ]
                                   is
28
                                         scalar\text{-}char\text{-}initialization\text{-}expr
29
30
    制約: extrinsic-spec-arg-listの中には、language、model、あるいは external-name の少なく
31
          とも1つが指定されなければならず、またどれも2回以上指定することはできない。
32
33
    制約: もし、LANGUAGE=を使わずに language を指定する場合、language は extrinsic-spec-arg-
34
          list中の最初の要素でなければならない。もし、MODEL=を使わずに model を指定する場
35
          合、LANGUAGE=のない language が extrinsic-spec-arg-list 中の最初の要素であり、model
36
37
         が2番目の要素でなければならない。もし、EXTERNAL_NAME=を使わずに external-name
38
          を指定する場合、LANGUAGE=のない language が extrinsic-spec-arg-list 中の最初の要素
39
          であり、MODEL=のない model が 2 番目の要素でなければならない。
40
41
    制約: LANGUAGE=、MODEL=、EXTERNAL_NAME=を伴う形は、上で禁止された場合を除いて、い
42
         かなる順番で書いても構わない。
43
          これらの extrinsic-spec-arq-list に関する規則は、あたかも EXTRINSIC が、LANGUAGE、
44
         MODEL、EXTERNAL_NAME という、それぞれが OPTIONAL であるような dummy-arg-list
45
46
          を使用した明示的引用仕様を伴った手続であるかのようなものであることに注意され
47
         たい。
```

制約: languageの中では、char-initialization-expr の値は、以下のものが許される。

● 'HPF' HPF 言語を指す。もし *model* が明示的に指定されていない場合、*model* には'GLOBAL'が暗黙に仮定される。

- 'FORTRAN' ANSI または ISO 規格の Fortran 言語を指す。もし *model* が明示的に 指定されていない場合、*model* には'SERIAL'が暗黙に仮定される。
- 'F77' 以前の ANSI または ISO 規格である FORTRAN 77言語を指す。もし *model* が明示的に指定されていない場合、*model* には'SERIAL'が暗黙に仮定される。
- 'C' ANSI 規格の C 言語を指す。もし *model* が明示的に指定されていない場合、 *model* には'SERIAL'が暗黙に仮定される。
- 実装依存の値。暗黙の model は実装に依存する。

ほとんどの実装にとって、'C'は、引用仕様本体 (*interface-body*) 中に記述された FUNC-TION 文か SUBROUTINE 文でしか許されないことに注意されたい。

制約: language が指定されていない場合、親有効域と同じものが仮定される。

制約: modelにおいて、char-initialization-expr の値は、以下のものが許される。

- 'GLOBAL' グローバルモデルを指す。
- 'LOCAL' ローカルモデルを指す。
- 'SERIAL' シリアルモデルを指す。
- 実装依存の値。

制約: model が指定されず、language の指定から暗黙に仮定されない場合、親有効域と同じものが仮定される。

制約: 名称が HPF の 3 文字から始まる全ての language 及び model は、本仕様及びその後継仕様の現在あるいは将来における定義のために予約されている。

制約: external-name において、scalar-char-initialization-expr の値は、その用途が外来種別によって決定される文字列である。例えば、ある外来種別は、external-name を、その手続が C の手続から参照された場合の名前を指定するために使用するかもしれない。そのような実装では、ユーザはコンパイラに、その名前が C コンパイラに理解できるように変換を行なうことを期待するであろう。もし external-name が指定されていない場合、その値は実装依存となる。

H614 extrinsic-kind-keyword is HPF

or HPF_LOCAL

or HPF_SERIAL

制約: EXTRINSIC(HPF) は EXTRINSIC('HPF', 'GLOBAL') と同値である。 extrinsic-prefix が存在しないとき、HPF コンパイラはコンパイル単位を外来種別 HPF に属するかのように解釈する。従って、HPF コンパイラにとって、EXTRINSIC(HPF) あるいは

EXTRINSIC('HPF', GLOBAL') と指定するのは冗長である。しかし、このような明示 1 的な指定は、複数の外来種別をサポートしているコンパイラを使用する場合に必要と 2 なるかもしれない。 3 制約: EXTRINSIC(HPF_LOCAL)は EXTRINSIC('HPF','LOCAL')と同値である。外来種別が 5 HPF LOCAL であるような主プログラムは、外来種別が HPF で、実行部がそのサブルー 6 チンの呼出しだけから構成された主プログラムから引数なしで呼び出される、外来種 7 8 別HPF_LOCALのサブルーチンであるかのように振る舞う。 9 制約: EXTRINSIC(HPF_SERIAL)は EXTRINSIC('HPF','SERIAL')と同値である。外来種別が 10 HPF_SERIAL であるような主プログラムは、外来種別が HPF で、実行部分がそのサブ 11 12 ルーチンの呼出しだけから構成された主プログラムから引数なしで呼び出される、外 13 来種別 HPF_SERIAL のサブルーチンであるかのように振る舞う。 14 制約: 名称が HPF の 3 文字から始まる全ての extrinsic-kind-keyword は、本仕様及びその後継 15 16 仕様の現在あるいは将来における定義のために予約されている。 17 18 A.8 データマッピングの公認拡張仕様 19 20 A.8.2 拡張データマッピング指示文の属性形式の構文 21 22 H801 combined-attribute-extended is ALIGN align-attribute-stuff 23 ${\bf or} \ \ {\tt DISTRIBUTE} \ \ dist-attribute-stuff \\$ 24 or INHERIT or TEMPLATE 26 or PROCESSORS 27 or DIMENSION (explicit-shape-spec-list) 28 29 or DYNAMIC 30 or RANGE range-attr-stuff 31 or SHADOW shadow-attr-stuff 32 or SUBSET 33 34 35 制約: SUBSET 属性は、プロセッサ構成にだけ適用できる。 36 37 38 A.8.3 REDISTRIBUTE 指示文

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 $distribute\,e\text{-}list$

制約: REDISTRIBUTE 指示文に現れる distributee は、DYNAMIC 属性をもたなければならない(8.5 節 参照)。

制約: REDISTRIBUTE 指示文の distributee は、ALIGN または REALIGN 指示文の alignee として現れてはならない。

制約: REDISTRIBUTE 指示文の dist-format-clause や dist-targetのどちらも、"*" で始まって はならない。 A.8.4 REALIGN 指示文 H803 realign-directive is REALIGN alignee align-directive-stuff \mathbf{or} REALIGN align-attribute-stuff:: alignee-list制約: REALIGN 指示文に現れる alignee は、DYNAMIC 属性をもたなければならない (8.5 節 9 10 参照)。 11制約: align-with-clauseに指定された align-targetが DYNAMIC 属性をもつ場合、aligneeも同 12 13 様に DYNAMIC 属性をもたなければならない。 14 制約:REALIGN 指示文の alignee は、DISTRIBUTE や REDISTRIBUTE 指示文の distributee で 15 あってはならない。 16 17 18 A.8.5 DYNAMIC 指示文 19 H804 dynamic-directive $\ \, \textbf{is} \ \, \, \textbf{DYNAMIC} \ \, a ligne \textit{e-or-distributee-list} \\$ 20 21 H805 aligne e-or-distribute ealigneeis 22 **or** distributee 23 24 制約: 共通ブロックの実体は、DYNAMICと宣言することはできず、また、DYNAMICである実 体(またはテンプレート)に整列することはできない。(ここで制約されているようなこ 26 27 とをしたいならば、共通プロックの代わりにモジュールを使わなければならない。) 28 制約: 構造型の成分は、POINTER 属性をもつ場合だけ DYNAMIC 属性をもつことができる。(詳 29 しくは、8.9節を参照されたい。) 30 31 制約: SAVE 属性をもつ実体は、DYNAMICと宣言することはできず、また、DYNAMICである実 32 体(またはテンプレート)に整列することはできない。 33 34 35 部分プロセッサへのマッピング A.8.736 H806 extended-dist-target is processors-name [(section-subscript-list)] 37**or** * processors-name [(section-subscript-list)] 39 or * 40 41制約: 部分配列添字並び(section-subscript-list)の部分配列添字(section-subscript)は、べ 42 クトル添字 (vector-subscript) であってはならず、添字 (subscript) または添字三つ組 43 (subscript-triplet) でなければならない。 44 45 制約: 部分配列添字並び (section-subscript-list) では、部分配列添字 (section-subscript) の数 46

は、processor-nameの次元数と等しくなければならない。

制約:	DISTRIBUTE 指示文の中では、それぞれの部分配列添字 $(section-subscript)$ は、宣言式 $(specification-expr)$ でなければならない。
制約:	DISTRIBUTE および REDISTRIBUTE 指示文の中で、 $dist$ - $format$ - $list$ と $dist$ - $target$ の両方が指定されたとき、"*"でない $dist$ - $format$ - $list$ の要素の数は、指定されたプロセッサ構成の添字三つ組 $(subscript$ - $triplet)$ の数と等しくなければならない。
制約:	DISTRIBUTE および REDISTRIBUTE 指示文の中で、 $dist$ -format-list がなく $dist$ -target が指定されたとき、それぞれの $distributee$ の次元数は、指定されたプロセッサ構成の添字三つ組 $(subscript$ -triplet) の数と等しくなければならない。
制約:	DISTRIBUTE 指示文で、 $dist$ - $format$ - $clause$ または $dist$ - $target$ のどちらかが "*" で始まるときは、すべての $distributee$ は、 $distributee$ が POINTER 属性をもつ場合を除き、仮引数でなければならない。
制約:	ALIGN 指示文の align-spec が "*" で始まるときは、すべての alignee は、alignee が POINTER 属性をもつ場合を除き、仮引数でなければならない。
制約:	inheritee は、aligneeが POINTER 属性をもつ場合を除き、仮引数でなければならない。
A.8.9	9 構造体成分のマッピング
H807	$\begin{array}{cccccccccccccccccccccccccccccccccccc$
制約:	構造型の成分は、その型が明示的にマップされていない場合に限って明示的に分散することができる。
制約:	構造型のデータ実体は、その構造型が明示的にマップされた型でない場合に限って明 示的に分散することができる。
制約:	DISTRIBUTE 指示文の distributee は、構造体成分 (structure-component) であってはならない。
制約:	構造型定義の中に現れる DISTRIBUTE 指示文の $distributee$ は、構造型の成分の成分名 $(component-name)$ でなければならない。
制約:	構造型定義の中に現れる ${ t DISTRIBUTE}$ 指示文に限って、 ${ t distributee}$ を成分名 $({ t component-name})$ とすることができる。
制約:	REDISTRIBUTE 指示文の中でだけ $distributee$ を構造体成分にすることができる。このとき、右端を除くすべての部分参照はスカラ $(0$ 次元) でなければならない。構造体成分の右端の部分参照は、DYNAMIC 属性をもっていなければならない。

H808	alignee-extended	is object-name	1
		or component-name	2
		or structure-component	3
			4
制約:	構造型の成分は、その型が明示 ることができる。	的にマップされていない場合に限って明示的に整列す	5 6 7
制約:	構造型のデータ実体は、その構 示的に整列することができる。	造型が明示的にマップされた型でない場合に限って明	8 9 10
制約:	ALIGN 指示文の alignee は、構造	\mathbb{E} 体成分 $(structure\text{-}component)$ であってはならない。	11 12
制約:	構造型定義の中に現れる \mathtt{ALIGN} \mathtt{f} \mathtt{name}) でなければならない。	旨示文の alignee は、構造型の成分の成分名 (component-	13 14 15
制約:	構造型定義の中に現れる ALIGN とすることができる。	指示文に限って、alignee を成分名 (component-name)	16 17
制約:	できる。このとき、右端を除く	nee を構造体成分 (structure-component) にすることが すべての部分参照はスカラ (0 次元) でなければならな 照は、DYNAMIC 属性をもっていなければならない。	18 19 20 21 22
H809	$align\-target\-extended$	is object-name	23
11005	awyn wryct catenaca	or template-name	24
		or component-name	25
		or structure-component	26
		or assert compensation	27
制約:	構造型定義の中に現れる ALIGN	指示文に限って、整列先を成分名 (component-name)	28 29
	とすることができる。		30
#11 <i>6</i> /5	1. , , , , , , , , , , , , , , , , , , ,	リスキストキースの七世ナ吸ノナベア	31
制約:		cture-component) であるとき、その右端を除くすべて	32
	の部分参照はスカラ (0 次元) で	は1716日からない。	33
A O -	10 年1177地元		34
A.8.1	10 新しい分散形式		35 36
H810	extended-dist-format	is BLOCK $[$ ($int ext{-}expr$) $]$	37
		$f or$ CYCLIC $[$ ($int ext{-}expr$) $]$	38
		${f or}$ GEN_BLOCK ($int ext{-}array$)	39

 ${f or}$ INDIRECT (int-array) or *

制約: DISTRIBUTE 指示文または REDISTRIBUTE 指示文の extended-dist-format の中に現れる 整数型配列 $(int ext{-}array)$ は、整数型の一次元配列でなければならない。

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制約: DISTRIBUTE 指示文の extended-dist-format の中に現れる整数型配列 (int-array) は、制 限式 (restricted-expr) でなければならない。

制約: GEN_BLOCK 分散に現れる整数型配列 (int-array) の大きさは、分散先のプロセッサ構成 1 の対応する次元の寸法と等しくなければならない。 2 3 制約: INDIRECT分散に現れる整数型配列 $(int ext{-}array)$ の大きさは、その分散が適用される $dis ext{-}$ 4 tributee の対応する次元の寸法と等しくなければならない。 5 6 7 A.8.11RANGE 指示文 8 H811 range-directive RANGE ranger range-attr-stuff is9 10 H812 ranger object-name is 11 \mathbf{or} template-name 12 13 H813 range-attr-stuff range-distribution-listis 14 H814 range-distribution (range-attr-list)is 15 16 H815 range-attr range-dist-formatis17 or ALL 18 H816 range-dist-format is BLOCK [()] 19 20 or CYCLIC [()] 21 or GEN_BLOCK 22 or INDIRECT 23 or * 24 26 27 制約: 少なくとも以下のいずれかが成り立たなければならない。 28 ● rangerは DYNAMIC 属性をもつ。 29 30 rangerはINHERIT属性をもつ。 31 • rangerはDISTRIBUTE指示文か combined-directive で指定され、その dist-format-32 clause は*である。 33 34 制約: range-attr-listの長さはそれぞれ、rangerの次元数と等しくなければならない。 35 36 制約: rangerは ALIGN 指示文または REALIGN 指示文の alignee であってはならない。 37 38 SHADOW 指示文 A.8.1239 40 H817 shadow-directive ${\tt SHADOW}\ shadow-target\ shadow-attr-stuff$ is 41 42 H818 shadow-targetobject-name is43 $\mathbf{or} \quad component\text{-}name$ 44 $H819 \quad shadow-attr-stuff$ (shadow-spec-list) 45 is

is

width

or low-width : high-width

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H820 shadow-spec

H821 width	is	int- $expr$	1
H822 low-width	is	int- $expr$	2 3
H823 high-width	is	int- $expr$	4
Med AL			5
		aとして現れる整数式 (int-expr) は、0 以上の値を	6
もつ宣言式 $(specification-expr)$	でな	ければならない。	7 8
			9
A.9 データとタスク並列に対する	5公記	忍拡張	10
A 0.1 、江動プロセッサ焦合			11
A.9.1 活動プロセッサ集合			12
${\rm H}901\ \ subset-directive$	is	SUBSET processors-name	13
			14 15
A.9.2 ON 指示文			16
H902 on-directive	ic	ON on stuff	17
11302 On-arrective	is	ON on-stuff	18
H903 on-stuff	is	$home \ [\ ext{, } resident\text{-}clause \] \ [\ ext{, } new\text{-}clause \]$	19
H904 on-construct	is		20
		$directive ext{-}origin \ block ext{-}on ext{-}directive$	21 22
		block	23
		$directive ext{-}origin \ end ext{-}on ext{-}directive$	24
H905 block-on-directive	is	ON on-stuff BEGIN	25
	15	ON On-staff BEGIN	26
H906 end-on-directive	is	END ON	27 28
H907 home	is	HOME (variable)	29
	or	HOME ($template\text{-}elmt$)	30
	or	($processors\text{-}elmt$)	31
H908 $template$ - $elmt$	is	$template-name \ [\ (\ section-subscript-list \) \]$	32
•			33
H909 $processors-elmt$	is	$processors-name \ [\ (\ section-subscript-list \) \]$	34 35
	1		36
A.9.3 RESIDENT 節、指示文、	構.	X	37
${\rm H910}\ resident\text{-}clause$	is	RESIDENT resident-stuff	38
H911 resident-stuff	is	[(res-object-list)]	39 40
H912 resident-directive	is	RESIDENT resident-stuff	41
			42
H913 $resident-construct$	is	,, ,, ,, ,, ,, ,, ,, ,, ,, ,, ,, ,, ,,	43
		$directive ext{-}origin \ block ext{-}resident ext{-}directive$	44 45
		block	46
		$directive ext{-}origin \ end ext{-}resident ext{-}directive$	47
H914 block-resident-directive	is	RESIDENT $resident\text{-}stuff$ BEGIN	48

H915	end-resident-directive	is	END RESIDENT
H916	res-object	is	object
A.9. 4	4 TASK_REGION 構文		
H917	task-region-construct	is	directive-origin block-task-region-directive block directive-origin end-task-region-directive
H918	$block\hbox{-} task\hbox{-} region\hbox{-} directive$	is	TASK_REGION
H919	$end\hbox{-} task\hbox{-} region\hbox{-} directive$	is	END TASK_REGION
A.10	非同期入出力に関する公認	拡張	
		or	ASYNCHRONOUS
		or or	<pre>ID = scalar-default-int-variable ASYNCHRONOUS</pre>
制約:	ASYNCHRONOUS 指定子又は ID=指 しなければならない。	〔定 子	そのどちらか一方を指定した場合、その両方を指定
制約:			易合、REC=指定子を指定しなければならず、かつ 変数群名」を指定してはならない。
制約:	ASYNCHRONOUS 指定子を指定した 引用してはならない。	た場合	合、データ転送文中のどの式においても、関数を
			<pre>ID = scalar-default-int-variable PENDING = scalar-default-logical-variable</pre>
制約:	INQUIRE 文中に FILE=指定子を てはならない。	指定	した場合、ID=指定子や PENDING=指定子を指定し
制約:	ID=指定子又は PENDING=指定子 ければならない。	のど	ちらか一方を指定する場合、その両方を指定しな
A.10	.1 WAIT文		
H1001	L wait-stmt	is	WAIT (wait-spec-list)
H1002	$2\ wait ext{-}spec$	is or or	$\begin{split} & \text{UNIT = } io\text{-}unit \\ & \text{ID = } scalar\text{-}default\text{-}int\text{-}expr \\ & \text{ERR = } label \end{split}$

 \mathbf{or} IOSTAT = label

制約: wait-spec-listには、UNIT=指定子をちょうど1つ、ID=指定子をちょうど1つと、他の 各指定子を高々1つ、指定しなければならない。 HPF 外来機能に関する公認拡張 外来言語との結合仕様 A.11.2 H1101 type-declaration-stmt-extended is type-spec [[, attr-spec-extended] ... ::] entity-decl-list10 H1102 attr-spec-extended PARAMETER 11or access-spec 12 or ALLOCATABLE 13 14 or DIMENSION (array-spec) 15 or EXTERNAL 16 or INTENT (intent-spec) 17 or INTRINSIC 18 or OPTIONAL 19 or POINTER 20 or SAVE 21 22 or TARGET 23 or MAP_TO (map-to-spec) 24 or LAYOUT (layout-spec) 25 or PASS_BY (pass-by-spec) 26 27 H1103 map-to-spec scalar-char-initialization-expr is28 H1104 layout-spec is scalar-char-initialization-expr29 30 $H1105\ pass-by-spec$ is scalar-char-initialization-expr31 32 33 制約: 同じ attr-spec-extended を、一つの type-declaration-stmt 中で 2 回以上指定してはなら 34 ない。 35 36 制約: 一つのデータ要素には、一つの有効域内で、どの属性も2回以上明示的に指定しては 37 ならない。 38 39 制約: MAP_TO 属性、LAYOUT 属性、及び PASS_BY 属性は、これらの属性が明示的に定義され ている外来種別の有効域内で、仮引数に対してだけ指定できる。 41 42

附属書B 構文記号の索引

この附属書は、構文規則で用いられる記号の索引を示す。H で始まる識別番号は、本 High Performance Fortran 言語仕様書の構文規則を表し、その規則の全体は附属書 A に示されている。R で始まる識別番号は、Fortran 言語規格 ("Fortran 95") の構文規則を表す。

B.1 構文規則の左辺に現れる非終端記号

記号	定義箇所	参照箇所
action-stmt	R216	H208
add- op	R710	H323 H505
$add ext{-}operand$	R706	H326 H505
$align\hbox{-} add\hbox{-} oper and$	H324	H323 H324
$align\hbox{-} attribute\hbox{-} stuff$	H315	H302 H801 H803
$align\hbox{-}directive$	H313	$H204\ H206$
$align\hbox{-}directive\hbox{-}stuff$	H314	H313 H803
$align\hbox{-}dummy$	H318	H317 H325
$align\hbox{-} primary$	H325	H324
$align\mbox{-}source$	H317	H314 H315
align-spec	H320	H319
$align\hbox{-}subscript$	H322	H320
$align\hbox{-}subscript\hbox{-}use$	H323	H322 H323 H325
$align\hbox{-} target$	H321	H320
$align\hbox{-} target\hbox{-} extended$	H809	
$align\hbox{-}with\hbox{-}clause$	H319	H314 H315
alignee	H316	H313 H803 H805
$alignee\hbox{-}extended$	H808	
$alignee\hbox{-} or\hbox{-} distributee$	H805	H804
$allocate \hbox{-} object$	R625	
$allocate ext{-}stmt$	R622	
and- op	R720	H505
$and \hbox{-} oper and$	R715	H505
$array\hbox{-} constructor$	R432	
array- $spec$	R513	H1102
assignment-stmt	R735	
$association\hbox{-}name$	H334	H333
$attr ext{-}spec$	R503	

$attr\-spec\-extended$	H1102	H1101	1
block	R801	H904 H913 H917	2
block- $data$ - $stmt$	H607		3
block-on- $directive$	H905	H904	4
$block ext{-}resident ext{-}directive$	H914	H913	5
block-task-region-directive	H918	H917	6 7
call-stmt	R1211		8
case-construct	R808	H208	9
combined - $attribute$	H302	H301	10
$combined\hbox{-} attribute\hbox{-} extended$	H801		11
combined-decl	H303	H301	12
$combined \hbox{-} directive$	H301	H204 H206	13
data- $stmt$	R532		14 15
$deallocate ext{-}stmt$	R631		16
directive- $origin$	H202	H201 H904 H913 H917	17
dist- $attribute$ - $stuff$	H307	H302 H801 H802	18
${\it dist-directive-stuff}$	H306	H305 H307 H802	19
dist- $format$	H310	H309	20
dist- $format$ - $clause$	H309	H306	21
dist- $onto$ - $clause$	H311	H306 H307	22 23
dist- $target$	H312	H311	24
$distribute \hbox{-} directive$	H305	H204 H206	25
distributee	H308	H305 H802 H805	26
distribute e-extended	H807		27
do-construct	R816	H208	28
dummy- arg	R1223	H602	29
$dynamic\hbox{-}directive$	H804	H206	30 31
$end\hbox{-}function\hbox{-}stmt$	R1220		32
$end\hbox{-} on\hbox{-} directive$	H906	H904	33
$end\hbox{-}resident\hbox{-}directive$	H915	H913	34
$end\hbox{-} subroutine\hbox{-} stmt$	R1224		35
$end\hbox{-} task\hbox{-} region\hbox{-} directive$	H919	H917	36
$entity ext{-}decl$	R504	H1101	37 38
equiv- op	R722	H505	39
$equiv\hbox{-} oper and$	R717	H505	40
$executable\hbox{-}construct$	R215		41
$executable\hbox{-} construct\hbox{-} extended$	H208		42
$executable\hbox{-} directive$	H205	H203	43
$executable\hbox{-} directive\hbox{-} extended$	H207		44
$execution\hbox{-}part$	R208		45 46
$explicit\hbox{-}shape\hbox{-}spec$	R514	H302 H303 H330 H332 H801	47
expr	R723	H505	48

1	$extended ext{-}dist ext{-}format$	H810	
2	$extended ext{-}dist ext{-}target$	H806	
3	external- $name$	H613	H610
4	extrinsic-kind-keyword	H614	H609
5	extrinsic-prefix	H608	H604 H605 H606 H607
6 7	extrinsic-spec	H609	H608
8	extrinsic-spec-arg	H610	H609
9	function-reference	R1210	
10	function-stmt	H601	
11	$function ext{-}subprogram$	R1216	
12	$high ext{-}width$	H823	H820
13	home	H907	H903
14 15	hpf- $directive$	H203	H201
16	hpf-directive-line	H201	
17	hpf-entity	H304	H303
18	if- $construct$	R802	H208
19	$independent ext{-}directive$	H501	H205 H207
20	$inherit\mbox{-}directive$	H401	H204 H206
21	inheritee	H402	H401
22 23	input- $item$	R914	
24	int- add - $operand$	H326	H323 H324
25	int- $expr$	R728	H310 H322 H810 H821 H822 H823
26	int-level- two - $expr$	H328	H323
27	$int ext{-}mult ext{-}operand$	H327	H324
28	$int ext{-}variable$	R607	H318
29	$interface ext{-}body$	R1205	
30 31	$internal\hbox{-} subprogram\hbox{-} part$	R210	
32	io-unit	R901	H1002
33	kind-selector	R506	
34	label	R313	H1002
35	language	H611	H610
36	$layout ext{-}spec$	H1104	H1102
37 38	level-2- $expr$	R707	H328 H505
39	$low ext{-}width$	H822	H820
40	map- to - $spec$	H1103	H1102
41	mask- $expr$	R743	
42	model	H612	H610
43	module- $stmt$	H606	
44	mult- op	R709	H505
45 46	mult- $operand$	R705	H327 H505
47	name list-stmt	R544	
48	new- $clause$	H502	H501 H903

nullify-stmt	R629		1
$on ext{-}construct$	H904	H208	2
$on ext{-}directive$	H902	H207	3
$on ext{-}stuff$	H903	H902 H905	4
or-op	R721	H505	5
or-operand	R716	H505	6
output-item	R915	11000	7 8
pass-by-spec	H1105	H1102	9
pointer-assignment-stmt	R736	111102	10
pointer-object	R630		11
prefix	H603	H601 H602	12
prefix-spec	H604	H603	13
processors-decl	H330	H329	14
processors-directive	H329	H204 H206	15
processors- $elmt$	H909	H907	16
program- $stmt$	H605	11501	17 18
	H815	H814	19
range-attr $range-attr-stuff$	H813	H801 H811	20
range-directive	H811	H206	21
range-dist-format	H816	H815	22
	H814	H813	23
range-distribution	но14 Н812		24
ranger		H811	25
read-stmt	R909	11907	26 27
realign-directive	H803	H207	28
redistribute-directive	H802	H207	29
reduction-clause	H503	H501	30
reduction-function	H506	H505	31
reduction-stmt	H505	IIFOO	32
reduction-variable	H504	H503	33
res-object	H916	H911	34
resident-clause	H910	H903	35 36
resident-construct	H913	H208	37
resident-directive	H912	H207	38
resident-stuff	H911	H910 H912 H914	39
section-subscript	R618	H806 H908 H909	40
$sequence \hbox{-} directive$	H333	H204 H206	41
shadow- $attr$ - $stuff$	H819	H801 H817	42
$shadow\mbox{-}directive$	H817	H206	43
shadow-spec	H820	H819	44 45
shadow-target	H818	H817	46
$specification\hbox{-} directive$	H204	H203	47
$specification\hbox{-}directive\hbox{-}extended$	H206		48

1	$specification\hbox{-}expr$	R734	
2	$specification\hbox{-}part$	R204	
3	$stat ext{-}variable$	R623	
4	stop-stmt	R840	
5 6	stride	R620	
7	structure-component	R614	H504 H807 H808 H809
8	subroutine-stmt	H602	
9	subscript	R617	
10	$subscript ext{-} triplet$	R619	H322
11	subset-directive	H901	H206
12	target	R737	
13 14	task-region-construct	H917	H208
15	template-decl	H332	H331
16	$template \hbox{-} directive$	H331	H204 H206
17	$template \hbox{-} elm t$	H908	H907
18	type-declaration-stmt	R501	
19	type-declaration-stmt-extended	H1101	
20	type-spec	R502	H604 H1101
21 22	variable	R601	H505 H907
23	$wait ext{-}spec$	H1002	H1001
24	$wait ext{-}stmt$	H1001	
25	where-construct	R739	H208
26	where-stmt	R738	
27	width	H821	H820
28	$write ext{-}stmt$	R910	
29			

B.2 構文規則の左辺に現れない非終端記号

34	記号	参照箇所
35	access-spec	H1102
36	$array ext{-}variable ext{-}name$	H504
37	$block ext{-}data ext{-}name$	H607
38	char-initialization-expr	H611 H612 H613 H1103 H1104 H1105
39 40	common-block-name	H334
41	$component{-}name$	H807 H808 H809 H818
42	$default ext{-}int ext{-}expr$	H1002
43	dummy-arg-name	H601
44	$function{-}name$	H601
45	int- $array$	H810
46	$intent ext{-}spec$	H1102
47	$module{-}name$	H606

object	H916						1
object-name	H303	H308	H316	H321	H334	H402	2
·	H807	H808	H809	H812	H818		3
processors-name	H304	H312	H330	H806	H901	H909	4
program-name	H605						5
result-name	H601						6 7
$subroutine{ ext{-}name}$	H602						8
template- $name$	H304	H308	H321	H332	H807	H809	9
	H812	H908					10
variable- $name$	H502	H504					11
							12
							13
B.3 終端記号							14 15
							16
記号	参照筐	師					17
!HPF\$	H202						18
(H302	H303	H309	H310	H314	H315	19
	H320	H325	H330	H332	H502	H503	20
	H505	H601	H602	H608	H801	H806	21
	H810	H814	H816	H819	H907	H908	22 23
	H909	H911	H1001	l H1102	2		24
)	H302	H303	H309	H310	H314	H315	25
	H320	H325	H330	H332	H502	H503	26
	H505	H601	H602	H608	H801	H806	27
	H810	H814	H816	H819	H907	H908	28
	H909	H911	H1001	l H1102	2		29
*	H309	H310	H312	H317	H320	H322	30 31
	H324	H505	H806	H810	H816		32
*HPF\$	H202						33
+	H505						34
,	H501	H505	H903	H1101	-		35
/	H334						36
:	H317	H820					37
::	H301	H333	H802	H803	H1101	L	38 39
=	H505	H611	H612	H613	H1002	2	40
ALIGN	H302	H313	H801				41
ALL	H815						42
ALLOCATABLE	H1102	2					43
BEGIN	H905	H914					44
BLOCK	H310	H607	H810	H816			45
CHPF\$	H202						46
CYCLIC	H310	H810	H816				47 48
							40

1	DATA	H607		
2	DIMENSION	H302	H801	H1102
3	DISTRIBUTE	H302	H305	H801
4	DYNAMIC	H801	H804	
5 6	ELEMENTAL	H604		
7	END	H906	H915	H919
8	ERR	H1002	2	
9	EXTERNAL	H1102	2	
10	EXTERNAL_NAME	H613		
11	EXTRINSIC	H608		
12	FUNCTION	H601		
13 14	GEN_BLOCK	H810	H816	
15	HOME	H907		
16	HPF	H614		
17	HPF_LOCAL	H614		
18	HPF_SERIAL	H614		
19	IAND	H506		
20	ID	H1002	?	
21 22	IEOR	H506		
23	INDEPENDENT	H501		
24	INDIRECT	H810	H816	
25	INHERIT	H302	H401	H801
26	INTENT	H1102	2	
27	INTRINSIC	H1102	2	
28	IOR	H506		
29 30	IOSTAT	H1002	2	
31	LANGUAGE	H611		
32	LAYOUT	H1102	2	
33	MAP_TO	H1102	2	
34	MAX	H506		
35	MIN	H506		
36	MODEL	H612		
37 38	MODULE	H606		
39	NEW	H502		
40	NO	H333		
41	ON	H902	H905	H906
42	ONTO	H311		
43	OPTIONAL	H1102	2	
44	PARAMETER	H1102	2	
45 46	PASS_BY	H1102	2	
47	POINTER	H1102	2	

PROCESSORS

H302 H329 H801

PROGRAM			
RANGE H801 H811 4 REALIGN H803 4 RECURSIVE H604 6 REDISTRIBUTE H802 7 REDUCTION H503 8 RESIDENT H910 H912 H914 H915 9 RESULT H601 102 11 SAVE H1102 11 SAUE H333 12 SHADOW H801 H817 11 SUBROUTINE H602 11 SUBSET H801 H901 110 11 TARGET H1102 17 TARGET H11001 17 WAIT H1001 17 WAIT H100	PROGRAM	H605	1
REALIGN H803	PURE	H604	2
REALIGN RECURSIVE REDISTRIBUTE REDISTRIBUTE RESIDENT RESIDENT RESIDENT RESULT R	RANGE	H801 H811	3
RECURSIVE H604 REDISTRIBUTE H802 7 REDUCTION H503 8 RESIDENT H910 H912 H914 H915 9 RESULT H601 H102 110 SAVE H1102 111 SUBROUTINE H801 H817 114 SUBROUTINE H602 115 SUBSET H801 H901 116 TARGET H1102 117 TASK_REGION H918 H919 115 TEMPLATE H302 H331 H801 119 UNIT H1002 20 WAIT H1001 210 WITH H319 213 WITH H319 213 24 25 26 26 27 28 29 29 30 30 30 30 30 30 30 30 30 30 30 30 30	REALIGN	H803	
REDISTRIBUTE REDUCTION H503 RESIDENT H910 H912 H914 H915 RESULT H601 SAVE H1102 11 SEQUENCE H333 SHADOW H801 H817 SUBROUTINE H602 15 SUBSET H801 H901 16 TARGET H1102 17 TASK_REGION H918 H919 18 TEMPLATE H302 H331 H801 19 UNIT H1002 20 WAIT H1001 21 WAIT H1001 22 WAIT H1001 22 WAIT H319 23 24 25 26 26 27 28 28 29 29 29 29 20 20 20 20 20 20 20 20 20 20 20 20 20	RECURSIVE	H604	
REDUCTION H503	REDISTRIBUTE	H802	
RESIDENT H910 H912 H914 H915 9 RESULT H601 10 SAVE H1102 11 SEQUENCE H333 12 SHADOW H801 H817 13 SUBROUTINE H602 15 SUBSET H801 H901 16 TARGET H1102 17 TASK_REGION H918 H919 18 TEMPLATE H302 H331 H801 19 UNIT H1002 20 WAIT H1001 21 WITH H319 22 WITH H319 23 WITH H319 23 WITH H319 23 WITH H319 33 WITH H3	REDUCTION	H503	
SAVE H1102 11 SEQUENCE H333 12 SHADOW H801 H817 13 SUBROUTINE H602 15 SUBSET H801 H901 16 TARGET H1102 17 TASK_REGION H918 H919 18 TEMPLATE H302 H331 H801 19 UNIT H1002 20 WAIT H1001 22 WITH H319 23 24 25 26 25 26 27 26 27 28 27 28 29 28 29 29 29 29 29 29 29 29 20 29 29 20 29 29 20 29 29 20 29 29 21 29 29 22 29 29 23 29 29 24 29 29 25 <td< td=""><td>RESIDENT</td><td>H910 H912 H914 H915</td><td></td></td<>	RESIDENT	H910 H912 H914 H915	
SEQUENCE H333 13 SHADOW H801 H817 14 SUBROUTINE H602 15 SUBSET H801 H901 16 TARGET H1102 17 TASK_REGION H918 H919 18 TEMPLATE H302 H331 H801 19 UNIT H1002 20 WAIT H1001 21 WITH H319 22 WITH H319 23 24 25 26 27 28 29 29 29 29 29 20 20 20 20 21 21 22 23 24 25 26 26 27 28 28 29 29 29 20 20 20 20 21 21 22 22 23 24 25 26 26 27 28 28 29 29 29 20 20 20 20 21 21 22 22 23 23 24 24 25 26 26 27 28 28 29 29 20 20 20 21 21 22 22 23 23 24 24 25 26 27 28 28 29 29 20 20 20 20 21 21 21 22 22 23 23 24 24 25 26 26 27 28 28 29 29 20 30 30 30 30 30 30 30 30 30 30 30 30 30	RESULT	H601	10
SEQUENCE SHADOW H801 H817 SUBROUTINE H602 15 SUBSET H801 H901 16 TARGET H1102 17 TASK_REGION H918 H919 18 TEMPLATE H302 H331 H801 19 UNIT H1002 WAIT H1001 WITH H319 21 WITH H319 22 WITH H319 23 24 25 26 26 27 28 29 29 30 30 30 31 31 31 31 31 31 31 31 31 31 31 31 31	SAVE	H1102	11
SHADOW SUBROUTINE SUBSET H801 H901 16 TARGET H1102 17 TASK_REGION H918 H919 18 TEMPLATE H302 H331 H801 19 UNIT H1002 WAIT H1001 22 WITH H319 23 24 24 25 26 27 28 29 29 29 29 20 30 30 31 31 31 31 32 33 34	SEQUENCE	H333	12
SUBROUTINE H602 15 SUBSET H801 H901 16 TARGET H1102 17 TASK_REGION H918 H919 18 TEMPLATE H302 H331 H801 19 UNIT H1001 22 WAIT H1001 22 WITH H319 23 26 26 27 28 28 29 30 30 31 32 32 33 33 34	SHADOW	H801 H817	
SUBSET H801 H901 16 TARGET H1102 17 TASK_REGION H918 H919 18 TEMPLATE H302 H331 H801 19 UNIT H1002 20 WAIT H1001 21 WITH H319 23 24 25 25 26 26 27 27 28 29 30 30 31 31 32 32 33 33 34	SUBROUTINE	H602	
TARGET H1102 17 TASK_REGION H918 H919 18 TEMPLATE H302 H331 H801 19 WAIT H1001 22 WITH H319 23 24 25 26 25 26 27 26 27 28 27 28 29 30 31 32 31 32 33 32 33 34	SUBSET	H801 H901	
TEMPLATE	TARGET	H1102	
UNIT H1002 20 WAIT H1001 22 WITH H319 23 43 44 45 46 47 47 48 48 48 48 48 48 48 48 48 48 48 48 48	TASK_REGION	H918 H919	18
WAIT H1001 WITH H319 22 WITH H319 23 24 25 26 27 28 29 30 31 31 32 33 34	TEMPLATE	H302 H331 H801	19
WAIT WITH H319 22 WITH H319 23 24 25 26 27 28 29 30 31 31 32 33 34	UNIT	H1002	20
H319 H319 24 25 26 27 28 29 30 31 32 33 34	WAIT	H1001	
24 25 26 27 28 29 30 31 32 33 34	WITH	H319	
25 26 27 28 29 30 31 31 32 33 33			
27 28 29 30 31 32 33 34			
28 29 30 31 31 32 33 34			26
29 30 31 32 33 34			27
30 31 32 33 34			28
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附属書C HPF 1.1 Subset

As part of the definition of the previous version of the High Performance Fortran language, HPF 1.1, a subset language was formally defined, based on the Fortran 77 language. The goal was to permit more rapid implementations of a useful subset of HPF that did not require full implementation of the new ANSI/ISO standard Fortran ("Fortran 90").

No subset language is defined as part of the current version, HPF 2.0. This Annex is included in the HPF 2.0 language document as a convenient summary of the HPF 1.1 Subset, which has served as a minimum requirement for HPF implementations.

C.1 Fortran 90 Features in the HPF 1.1 Subset

The features of the HPF 1.1 subset languages are listed below. For reference, the section numbers from the Fortran 90 standard are given along with the related syntax rule numbers:

- All FORTRAN 77 standard conforming features, except for storage and sequence association.
- The Fortran 90 definitions of MIL-STD-1753 features:
 - DO WHILE statement (8.1.4.1.1 / R821)
 - END DO statement (8.1.4.1.1 / R825)
 - IMPLICIT NONE statement (5.3 / R540)
 - INCLUDE line (3.4)
 - scalar bit manipulation intrinsic procedures: IOR, IAND, NOT, IEOR, ISHFT,
 ISHFTC, BTEST, IBSET, IBCLR, IBITS, MVBITS (13.13)
 - binary, octal and hexadecimal constants for use in DATA statements (4.3.1.1 / R407 and 5.2.9 / R533)
- Arithmetic and logical array features:
 - array sections (6.2.2.3 / R618-621)
 - * subscript triplet notation (6.2.2.3.1)
 - * vector-valued subscripts (6.2.2.3.2)
 - array constructors limited to one level of implied DO (4.5 / R431)
 - arithmetic and logical operations on whole arrays and array sections (2.4.3, 2.4.5, and 7.1)

- array assignment (2.4.5, 7.5, 7.5.1.4, and 7.5.1.5)
- masked array assignment (7.5.3)
 - * WHERE statement (7.5.3 / R738)
 - * block WHERE . . . ELSEWHERE construct (7.5.3 / R739)
- array-valued external functions (12.5.2.2)
- automatic arrays (5.1.2.4.1)
- ALLOCATABLE arrays and the ALLOCATE and DEALLOCATE statements (5.1.2.4.3, 6.3.1 / R622, and 6.3.3 / R631)

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- assumed-shape arrays (5.1.2.4.2 / R516)

• Intrinsic procedures:

The list of intrinsic functions and subroutines below is a combination of (a) routines which are entirely new to Fortran and (b) routines that have always been part of Fortran, but have been extended here to new argument and result types. The new or extended definitions of these routines are part of the subset. If a FORTRAN 77 routine is not included in this list, then only the original FORTRAN 77 definition is part of the subset.

For all of the intrinsics that have an optional argument DIM, only actual argument expressions for DIM that are initialization expressions are part of the subset. The intrinsics with this constraint are marked with †in the list below.

- the argument presence inquiry function: PRESENT (13.10.1)
- all the numeric elemental functions: ABS, AIMAG, AINT, ANINT, CEILING, CMPLX, CONJG, DBLE, DIM, DPROD, FLOOR, INT, MAX, MIN, MOD, MODULO, NINT, REAL, SIGN (13.10.2)
- all mathematical elemental functions: ACOS, ASIN, ATAN, ATAN2, COS, COSH, EXP,
 LOG, LOG10, SIN, SINH, SQRT, TAN, TANH (13.10.3)
- all the bit manipulation elemental functions: BTEST, IAND, IBCLR, IBITS, IBSET,
 IEOR, IOR, ISHFT, ISHFTC, NOT (13.10.10)
- all the vector and matrix multiply functions: DOT_PRODUCT, MATMUL (13.10.13)
- all the array reduction functions: ALL†, ANY†, COUNT†, MAXVAL†, MINVAL†,
 PRODUCT†, SUM†(13.10.14)
- all the array inquiry functions: ALLOCATED, LBOUND†, SHAPE, SIZE†,
 UBOUND†(13.10.15)
- all the array construction functions: MERGE, PACK, SPREAD†, UNPACK (13.10.16)
- the array reshape function: RESHAPE (13.10.17)
- all the array manipulation functions: CSHIFT[†], EOSHIFT[†], TRANSPOSE (13.10.18)
- all array location functions: MAXLOC[†], MINLOC[†](13.10.19)

all intrinsic subroutines: DATE_AND_TIME, MVBITS, RANDOM_NUMBER, RANDOM_SEED,
 SYSTEM_CLOCK (3.11)

• Declarations:

- Type declaration statements, with all forms of type-spec except kind-selector and TYPE(type-name), and all forms of attr-spec except access-spec, TARGET, and POINTER. (5.1 / R501-503, R510)
- attribute specification statements: ALLOCATABLE, INTENT, OPTIONAL, PARAMETER,
 SAVE (5.2)

• Procedure features:

- INTERFACE blocks with no generic-spec or module-procedure-stmt (12.3.2.1)
- optional arguments (5.2.2)
- keyword argument passing (12.4.1 /R1212)

• Syntax improvements:

- long (31 character) names (3.2.2)
- lower case letters (3.1.7)
- use of " $_$ " in names (3.1.3)
- "!" initiated comments, both full line and trailing (3.3.2.1)

C.2 HPF 1.1 Directives and Language Extensions in the HPF 1.1 Subset

The following HPF 1.1 directives and language extensions to Fortran 90 were included in the HPF 1.1 Subset:

- The basic data distribution and alignment directives: ALIGN, DISTRIBUTE, PROCESSORS, and TEMPLATE.
- The forall-statement (but not the forall-construct).
- The INDEPENDENT directive.
- The SEQUENCE and NO SEQUENCE directives.
- The system inquiry intrinsic functions NUMBER_OF_PROCESSORS and PROCESSORS_SHAPE.
- The computational intrinsic functions ILEN, and the HPF extended Fortran intrinsics MAXLOC and MINLOC, with the restriction that any actual argument expression corresponding to an optional DIM argument must be an initialization expression.

For a discussion of the rationale by which features were chosen for the HPF 1.1 Subset, please consult HPF Language Specification Version 1.1.

附属書D Previous HPFF Acknowledgments

The current HPF 2.0 document would not have been possible without the contributions of the previous series of HPFF meetings. Following are the acknowledgments for those efforts.

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- Mary Zosel, Head of the Fortran 90 and Storage Association Subgroup;
- Guy Steele, Head of the Data Distribution Subgroup;
- Rob Schreiber, Head of the Intrinsics Subgroup;
- Bob Knighten, Head of the Parallel I/O Subgroup;
- Marc Snir, Head of the Extrinsics Subgroup;
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HPFF operated on a very tight budget (in reality, it had no budget when the first meeting was announced). The first meeting in Houston was entirely financed from the conferences budget of the Center for Research on Parallel Computation, an NSF Science and Technology Center. DARPA and NSF have supported research at various institutions that have made a significant contribution towards the development of High Performance Fortran. Their sponsored projects at Rice, Syracuse, and Yale Universities were particularly influential in the HPFF process. Support for several European participants was provided by ESPRIT through projects P6643 (PPPE) and P6516 (PREPARE).

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- Alok Choudhary, Head of Parallel I/O Subgroup;
- Chuck Koelbel, Head of Irregular Distributions Subgroup;
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関連図書

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- [3] High Performance Fortran Forum. *High Performance Fortran Language Specification* Scientific Programming, 2,1, 1993. Also published as: CRPC-TR92225, Center for Research on Parallel Computation, Rice University, Houston, TX, 1992 (revised May. 1993). Also published as: Fortran Forum, 12,4, Dec. 1993 and 13,2, June 1994.
- [4] High Performance Fortran Forum. High Performance Fortran Language Specification, version 1.0 CRPC-TR92225, Center for Research on Parallel Computation, Rice University, Houston, TX, 1992 (revised May. 1993).
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附属書E Policy and Mechanism for Recognized Extrinsic Interfaces

HPF defines certain extrinsics such as HPF_LOCAL, and HPF_SERIAL as interfaces that HPFF believes are useful to the HPF community. But there are many more such extrinsic interfaces beyond those maintained by HPFF. HPFF has a adopted a policy of formally recognizing certain extrinsic interface definitions, where the interface, and its addition to the HPF document is considered to be a service to the HPF community. Examples are language bindings to HPF or library packages.

E.1 Extrinsic Policy

To be considered for HPFF recognition, a proposed extrinsic must demonstrate the following things. It should be noted, however, that meeting these criteria does not guarentee acceptance of a proposed interface by HPFF.

- conformance to HPF rules for calling extrinsics,
- significant new functionality,
- existing practice such as users, implementations, etc.,
- institutional backing with evidence of ongoing support,
- coherent documentation,
- non-proprietary interface definition, and
- copyright goes to HPFF for interface, with permission to use (royalty free).

If a proposed extrinsic is accepted by HPFF, then:

- HPFF will recogize the interface and reference it in documentation, but HPFF does not assume responsibility for the extrinsic or its interface.
- The sponsor of the extrinsic must continue to conform to the HPF interface rules for extrinsics. The interface HPFF approves must not change without HPFF approval.
- The sponsor must assume responsibility for any CCI requests concernting the extrinsic.

A list of recognized extrinsic interfaces will be included in HPF documentation, with the following guidelines:

- There should be a single page introduction to the extrinsic which contains:
 - the name of the extrinsic,
 - a brief abstract of functionality,
 - a brief and informal description of the interface,
 - information about platform and system availability, and
 - reference and contacts for formal documentation, continued responsibility, and additional information (e.g. compiler availability).

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- There should be about two pages with short examples of usage.
- A short paper with the formal definition of the interface and an informal description of the functionality of the extrinsic.

E.2 Extrinsic Interface Mechanism

The HPF www-home page will have instructions for submission of an extrinsic interface. For HPFF consideration, the sponsor prepares a proposal that includes:

- a statement of what significant new functionality is provided,
- a description of existing practice,
- a statement of institutional backing with evidence of ongoing support,
- a copy of the complete documentation or a reference to an online version of the documentation,
- a draft of the text (described above) that would be included in the HPFF documentation, and
- a statement justifying the claim that the interface follows HPF conventions for calling extrinsics.

If the proposed extrinsic interface is approved by HPFF, the sponsor then submits:

- a formal statement for HPFF records that the interface definition is non-proprietary and that the copyright of the interface belongs HPFF,
- the formal contact for CCI and continued maintenance of the interface, and
- a copy of the interface documentation formatted for HPFF use, including a copy in the current document and web mark-up languages.

附属書F HPF_CRAFT

HPF_CRAFT is a hybrid language, combining an SPMD execution model with high performing HPF features. The model combines the multi-threaded execution of HPF_LOCAL and the HPF syntax. The goal of HPF_CRAFT is to attain the potential performance of an SPMD programming model with access to HPF features and a well-defined extrinsic interface to HPF.

F.1 Introduction

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HPF_CRAFT is a hybrid language, combining an SPMD execution model with high performing and portable HPF features. The model combines the multi-threaded execution of HPF_LOCAL and the HPF syntax and features. The goal of HPF_CRAFT is to attain the potential performance of an SPMD programming model with access to HPF features and a well-defined extrinsic interface to HPF. It is built on top of the HPF_LOCAL extrinsic environment.

SPMD features and a multi-threaded model allow the user to take advantage of the performance and opportunity for low level access of a more general purpose programming model. Including HPF data distribution features gives the programmer access to high performing aspects of both models, but with the added responsibility of working with a more low-level execution model. HPF_CRAFT is best suited for platforms that support one way communication features, but is consistent with HPF and easily targeted for platforms that have HPF and can support SPMD programming styles.

The HPF features included in HPF_CRAFT are a subset of the full HPF language chosen for their performance and their broad portability and ease of use. HPF_CRAFT contains additional features to support SPMD programming styles. There are some differences from HPF, however. For example, I/O causes differences; in HPF_CRAFT different processors are allowed to read from different files at the same time, in HPF the processors must all read from the same file. The differences in the models are principally caused by the multi-threaded execution model and the introduction of HPF_LOCAL data rules.

HPF_CRAFT allows for the notion of private data. Data defaults to a mapping in which data items are allocated so that each processor has a unique copy. The values of the individual data items and the flow of control may vary from processor to processor within HPF_CRAFT. This behavior is consistent with the behavior of HPF_LOCAL. In HPF_CRAFT a processor may be individually named and code executed based upon which processor it is executing on. HPF_CRAFT also allows for the notion of private loops. A private loop is executed in entirety by each processor.

The rules governing the interface to HPF_CRAFT subprograms are similar to those for the HPF_LOCAL interface. Dummy arguments use a hybrid of the interfaces between HPF and itself and that of HPF and HPF_LOCAL. Explicitly mapped dummy arguments behave just as they do in HPF, while default (private) dummy arguments use the HPF_LOCAL calling convention.

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HPF_CRAFT will be initially made available on Cray MPP systems and may also be available on Cray vector architectures. Future versions of HPF_CRAFT are possible on other vendor's architectures as well.

HPF_CRAFT is being implemented for Cray Research by The Portland Group, Inc. For Cray systems, HPF_CRAFT may be obtained through the Cray Research Inc. Orderdesk,

Cray Research Inc. orderdsk@cray.com (612) 683-5907

Additional formal documentation, requests, and suggestions can be made to

The Portland Group 9150 SW Pioneer Ct., Suite H Wilsonville, OR 97070 (503) 682-2806 trs@pgroup.com

F.2 Examples of Use

HPF_CRAFT is intended for use in circumstances where greater control and performance are desired for MIMD style architectures. Since data may be declared to be private, local control is made more available and since processor information is available message passing and direct memory access programming styles can be seamlessly integrated with explicitly mapped data.

The following examples show some of the capabilities of HPF_CRAFT that are different from those of HPF. Others, such as integrated message passing and synchronization primitives are not shown. Much of HPF can also be used within HPF_CRAFT.

Example 1 illustrates the difference between the default distribution for data and the distribution of mapped data.

! Example 1

INTEGER PRIVATE_A(100, 20), PRIVATE_B(12, 256), PRIVATE_C
INTEGER MAPPED_A(100, 20), MAPPED_B(12, 256), MAPPED_C
!HPF\$ DISTRIBUTE MAPPED_A(BLOCK, BLOCK), MAPPED_B(BLOCK, *), MAPPED_C

In the above example, given 8 processors, there would be 8 * 100 * 20 (or 16,000) elements in the array PRIVATE_A. Each processor contains an entire array named PRIVATE_A. The elements of PRIVATE_A on processor 1 cannot be referenced using implicit syntax by any other processor. There are only 100 * 20 (or 2000) elements of array MAPPED_A, however, and these elements are distributed about the machine in a (BLOCK, BLOCK) fashion.

The difference between the PRIVATE_A declaration in HPF_CRAFT and that in HPF is the most instructive. In HPF_CRAFT each processor contains one copy of the array, and the values of the elements of the array may vary from processor to processor. HPF implementations are permitted to make one copy of the array per processor the default, but the values of these copies must remain coherent across all processors. In HPF there is no way to write a conforming program in which different processors have different values for the same array.

Example 2 shows the usefulness of the ON clause for the INDEPENDENT loop as well as giving an example of how private data may be used.

```
PRIVATE_C = 0
!HPF$ INDEPENDENT (I, J) ON MAPPED_B(I, J)

DO J=1,256

DO I=1,12

MAPPED_B(I, J) = MAPPED_B(I, J) + 5

PRIVATE_C = PRIVATE_C + MAPPED_B(I, J)

ENDDO

ENDDO
```

 In this example, each iteration is executed on the processor containing the data that is mapped to it. The user was allowed to specify this.

In addition, the private variable PRIVATE_C is used to compute a total for each processor. At the end of execution of the loop, the values of PRIVATE_C may be different on each processor depending upon the values in the elements of the array on each processor. This data may be used as is, or it can be quickly summed using a barrier or an ATOMIC UPDATE.

Example 3 shows the final total value being combined into the variable MAPPED_C whose value is available to all processors.

```
! Example 3

MAPPED_C = 0
!HPF$ ATOMIC UPDATE

MAPPED_C = MAPPED_C + PRIVATE_C
```

Example 4 shows how the language allows private data to vary from processor to processor.

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```
! Example 4
```

```
IF (MY_PE() .EQ. 5) THEN
    PRIVATE_C = some-big-expression
ENDIF
```

In this example, PRIVATE_C on processor 5 will have the result of *some-big-expression*. Each processor can do distinctly different work and communicate through mapped data.

The code fragment in Example 5 is from an application and shows a few features of the language.

```
! Example 5
```

```
!HPF$ GEOMETRY G(*, CYCLIC)
      REAL FX(100,100), FY(100,100), FZ(100,100)
!HPF$ DISTRIBUTE (G) :: FX,FY,FZ
      REAL FXP(100,16,100), FYP(100,16,100)
!HPF$ DISTRIBUTE FXP(*,*, BLOCK) FYP(*,*, BLOCK)
      INTEGER CELL, ATOM, MAP(1000), NACELL(1000)
!HPF$ INDEPENDENT (CELL) ON FX(1,CELL)
      DO CELL=1,100
         JCELLO = 16*(CELL-1)
        DO NABOR = 1, 13
           JCELL = MAP(JCELLO+NABOR)
           DO ATOM=1, NACELL(CELL)
              FX(ATOM, CELL) = FX(ATOM, CELL) + FXP(ATOM, NABOR, JCELL)
              FY(ATOM, CELL) = FY(ATOM, CELL) + FYP(ATOM, NABOR, JCELL)
           ENDDO
        ENDDO
      ENDDO
```

The GEOMETRY directive allows the user to generically specify a mapping and use it to apply to many arrays (they need not have the same extents.)

Example 5 has a single INDEPENDENT loop which is the outer loop. It executes 100 iterations total. Within this loop the private value of JCELLO is set for each processor (ensuring that it is a local computation everywhere.) Nested inside the INDEPENDENT loop is a private loop; this loop executes 13 times *per* processor. Inside this loop JCELL is

computed locally on each processor, minimizing unnecessary communication. Finally the innermost loop is also private.

F.3 External Interface

This section describes the behavior when an HPF_CRAFT routine is called from HPF.

The calling convention and argument passing rules for HPF_CRAFT are a hybrid of those for HPF calling HPF_LOCAL and HPF calling HPF. Explicit interfaces are required. Where dummy arguments are private (default) storage, the HPF calling HPF_LOCAL conventions are used. Where dummy arguments are explicitly mapped, the calling convention matches HPF calling HPF.

There are a number of constraints on HPF_CRAFT routines that are called from HPF. The following is a list of restrictions placed on HPF_CRAFT routines called from HPF:

- Recursive HPF_CRAFT routines cannot be called from HPF.
- HPF_CRAFT routines called from HPF may only enter the routine at a single place (no alternate entries).
- An HPF_CRAFT supprogram may not be invoked directly or indirectly from within the body of a FORALL construct or within the body of an INDEPENDENT DO loop that is inside an HPF program.
- The attributes (type, kind, rank, optional, intent) of the dummy arguments in a supprogram called by HPF must match the attributes of the corresponding dummy arguments in the explicit interface.
- A dummy argument of an HPF_CRAFT supprogram called by HPF
 - must not be a procedure name.
 - must not have the POINTER attribute.
 - must not be sequential, unless it is also PE_PRIVATE.
 - must have assumed shape even when it is explicit shape in the interface.
 - if scalar, it must be mapped so that each processor has a copy of the argument.
- The default mapping of scalar dummy arguments and of scalar function results when an HPF program calls an HPF_CRAFT routine is that it is replicated on each processor.

If a dummy argument of an EXTRINSIC ('HPF_CRAFT') routine interface block is an array and the dummy argument of the HPF_CRAFT supprogram has the default private mapping, then the corresponding dummy argument in the specification of the HPF_CRAFT procedure must be an array of the same rank, type, and type parameters. When the extrinsic procedure is invoked, the dummy argument is associated with the local array that consists of the subgrid of the global array that is stored locally.

If the dummy argument of the HPF_CRAFT supprogram is explicitly mapped, it must have the same mapping as the dummy argument of the EXTRINSIC('HPF_CRAFT') supprogram. Note that this restriction does not require actual and dummy arguments to match and is no more stringent than saying that mappings of dummy arguments in interface blocks must match those in the actual routine.

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F.4 Execution Model

HPF_CRAFT is built upon the fundamental execution model of HPF_LOCAL, augmented with data mapping and work distribution features from HPF. It is also augmented with explicit low-level control features, many taken from Cray Research's CRAFT language.

In HPF_CRAFT there is a single task on each processor and all tasks begin executing in parallel, with data defaulting to a private distribution, the same default distribution used in HPF_LOCAL. Each processor gets a copy of the data storage unless specified otherwise by the user. Consequently I/O works identically to I/O in HPF_LOCAL and message passing libraries are easily integrated.

Simply stated, the execution model is that of HPF_LOCAL.

To provide correct behavior when explicitly mapped data is involved, this model defines implicit barrier points at which the execution model requires that all processors must stop and wait for the execution of all other processors before continuing. These barriers add additional semantics to the HPF_LOCAL behavior. An implementation may remove any of these barriers that are deemed unnecessary, but *every* processor must participate in the barriers at each one of these points.

The points where there are implicit barriers are conceptually after those instances in which the processors in the HPF_CRAFT program are executing cooperatively, as if in an HPF program (e.g., after an INDEPENDENT loop). An HPF_CRAFT program treats operations on explicitly mapped objects as if they were operations in an HPF program and it treates operations on private data as if they were executed within the HPF_LOCAL framework. It is occasionally useful for an advanced programmer to indicate to the compilation system where barriers are not needed; HPF_CRAFT has syntax to allow this capability.

F.5 HPF_CRAFT Functional Summary

HPF_CRAFT contains a number of features not available in HPF, and restricts the usage of many of the features currently available. The following is a concise list of the differences.

- INDEPENDENT has been extended to better support an ON clause.
- There are new rules defining the interaction of explicitly mapped and private data.
- Parallel inquiry intrinsics IN_PARALLEL() and IN_INDEPENDENT() have been added.
- Serial regions (MASTER / END MASTER) have been added.
- Explicit synchronization primitives are provided.

- The ATOMIC UPDATE, SYMMETRIC, and GEOMETRY directives have been added.
- Many other compiler information directives have been added to assist the compiler in producing good quality code.

F.5.1 Data Mapping Features

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Data mapping features provided are those that have been found useful most often. When data is explicitly mapped, only one copy of the data storage is created unless the explicit mapping directs otherwise. The value of explicitly mapped replicated data items must be consistent between processors as is the case in HPF. Storage and sequence association for explicitly mapped arrays is not guaranteed in HPF_CRAFT. For private data, storage and sequence association follows the Fortran 90 rules.

A new directive is included for completeness: PE_PRIVATE, which specifies that the data should conform to the default behavior. The values of private variables may vary on different processors.

F.5.2 Subprogram Interfaces

The behavior and requirements of an HPF_CRAFT program at subprogram interfaces may be divided into three cases. Each case is also available using some combination of HPF and HPF_LOCAL. For dummy arguments that are explicitly mapped, the behavior is identical to that of HPF. All processors must cooperate in a subprogram invocation that remaps or explicitly maps data. In other words, if an explicit interface is required (by the HPF rules) or the subprogram declares explicitly mapped data, the subprogram must be called on all processors. Processors need not cooperate if there are only reads to non-local data. The INHERIT attribute may only be applied to explicitly mapped data.

Data that has the default private mapping (case two) the behavior of an HPF_CRAFT subprogram at subprogram interfaces is identical to that of HPF_LOCAL. Data is passed individually on every processor and the processors need not interact in any way.

When a subprogram is passed actual arguments that are a combination of both explicitly mapped data and private data, the explicitly mapped data follows the HPF rules and the private data follows the HPF_LOCAL rules.

In case three, the user has the option of passing data with explicitly mapped actual arguments to dummy arguments that are not explicitly mapped (i.e., private.) The mapping rules for this data are identical to the mapping rules when HPF calls an HPF_LOCAL subprogram. The data remains "in-place." All HPF arrays are logically carved up into pieces; the HPF_CRAFT procedure executing on a particular physical processor sees an array containing just those elements of the global array that are mapped to that physical processor. There is implicit barrier synchronization after an INDEPENDENT loop. Transfer of control into or out of an INDEPENDENT loop is prohibited.

Finally, it is undefined behavior when an actual argument is private and the dummy argument is explicitly mapped. A definition could be supplied for this interaction, but

it is the same solution that one might propose for a calling sequence when HPF_LOCAL subprograms call HPF subprograms.

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F.5.3 The INDEPENDENT Directive

The INDEPENDENT directive is part of HPF_CRAFT with the same semantics as in HPF. However, within INDEPENDENT loops the values of private data may vary from processor to processor. INDEPENDENT applied to FORALL has identical syntax and semantics as in HPF.

An HPF independent loop optionally may have a NEW clause. The NEW clause is not required by HPF_CRAFT for default (not explicitly mapped) data. In HPF_CRAFT data defaults to private so values may differ from processor to processor.

Private data has slightly different behavior than data specified in the NEW clause. The value of a private datum on each processor can be used beyond a single iteration of the loop. Private data may be used to compute local sums, for example. The values of data items named in a NEW clause may not be used beyond a single iteration. The NEW clause asserts that the INDEPENDENT directive would be valid if new objects were created for the variables named in the clause for each iteration of the loop. The semantics of the NEW clause are identical in HPF_CRAFT and HPF.

The semantics of an INDEPENDENT applied to loops containing private data references changes with respect to the private data. The change can be summarized to say that instead of indicating that iterations have no dependencies upon one-another, with respect to the private data, iterations on different processors have no dependencies upon one-another.

F.5.4 The ON Clause

In addition to the version of INDEPENDENT available from HPF, a new version of INDEPENDENT is included that incorporates the ON clause. There are a number of differences between the versions of INDEPENDENT with and without the ON clause.

The new version of the INDEPENDENT directive may be applied to the first of a group of tightly nested loops and may apply to more than one of them. This more easily facilitates the use of the ON clause. The current INDEPENDENT directive applies only to a single loop nest. The INDEPENDENT directive is extended so that multiple loop nests can be named. The general syntax for these new independent loops is as follows:

```
!HPF$ INDEPENDENT (I_1,I_2,\ldots,I_n) ON array\text{-}name(h_1(I_1),h_2(I_2),\ldots,h_n(I_n)) DO I_1=L_1,\ U_1,\ S_1 DO I_2=L_2,\ U_2,\ S_2 ... DO I_n=L_n,\ U_n,\ S_n ... END DO ... END DO END DO
```

The syntax and semantics of INDEPENDENT with the ON clause are different from its syntax and semantics without the ON clause. With the ON clause the directive states that there are no cross-processor dependencies, but there may be dependencies between iterations on a processor. There is an implicit barrier synchronization after an INDEPENDENT loop. Transfer of control into or out of an INDEPENDENT loop is prohibited.

The iteration space of an INDEPENDENT nest must be rectangular. That is, the lower loop bound, the upper loop bound, and the step expression for each loop indicated by the INDEPENDENT induction list must be invariant with regard to the INDEPENDENT nest. Each index expression of array-name in the ON clause (the functions h_i above,) must be one of the following two forms:

```
[ a * loop\_control\_variable + ] b
[ a * loop\_control\_variable - ] b
```

where a and b must be integer values; they can be expressions, constants, or variables. The values of a and b must be invariant with regard to the INDEPENDENT loop nest. For example, specifying A(I,J,K) is valid. Specifying A(3,I+J,K) is not valid. Specifying A(I,I,K) is not valid because I appears twice. Division is prohibited in any index expression of the ON clause.

F.5.5 Array Syntax

Array syntax is treated identically in HPF_CRAFT as in HPF for explicitly mapped objects. For private objects the behavior is identical to that of HPF_LOCAL. When private objects and explicitly mapped objects are combined the rules are as follows:

```
result = rhs_1 \text{ op}_1 rhs_2 \text{ op}_2 \dots \text{ op}_m rhs_n
```

- If result is explicitly mapped and all rhs arrays are explicitly mapped, the work is distributed as in HPF.
- If result is private and all rhs arrays are private the computation is done on all processors as an HPF_LOCAL program would do it.
- If result is private and all rhs arrays are explicitly mapped, the work is distributed as in HPF and the values of the results are broadcast to the result on each processor.
- If result is explicitly mapped and not all rhs arrays are explicitly mapped, the results of the operation are undefined, unless all corresponding elements of all private rhs arrays have the same values.
- If result is private and some, but not all rhs arrays are explicitly mapped, the value is computed on each processor and saved to the local result.

All processors must participate in any array syntax statement in which the value of an explicitly mapped array is modified, and there is implicit barrier synchronization after the statement executes.

F.5.6 Treatment of FORALL and WHERE Statements

The FORALL and WHERE statements are treated exactly as in HPF when data is explicitly mapped. When private data is modified, the statement is executed separately on each processor. Finally, when data in a FORALL or WHERE are mixed, the rules for array syntax apply. If any explicitly mapped data item is modified in a forall-stmt or where-stmt then arrays in the forall-header or where-header must be explicitly mapped. In a FORALL construct, if any explicitly mapped array is modified, all modified arrays must be explicitly mapped. There is an implicit barrier synchronization after FORALL and WHERE statements if any arrays in the forall-header or where-header are explicitly mapped.

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F.5.7 Synchronization Primitives

A number of synchronization primitives are provided. These primitives include:

```
Barriers (test, set, wait)
Locks (test, set, clear)
Critical Sections
Events (test, set, wait, clear)
```

Barriers provides an explicit mechanism for a task to indicate its arrival at a program point and to wait there until all other tasks arrive. A task may test and optionally wait at an explicit barrier point. In the following example, a barrier is used to make sure that block3 is not entered by any task until all tasks have completed execution of block1.

```
block1
CALL SET_BARRIER()
block2
CALL WAIT_BARRIER()
block3
```

The following example performs a similar function as above. However, while waiting for all tasks to arrive at the barrier, the early tasks perform work within a loop.

```
block1
CALL SET_BARRIER()
DO WHILE (.NOT. TEST_BARRIER())
block2
END DO
block3
```

Locks are used to prevent the simultaneous access of data by multiple tasks.

The SET_LOCK(lock) intrinsic sets the mapped integer variable lock atomically. If the lock is already set, the task that called SET_LOCK is suspended until the lock is cleared by another task and then sets it. Individual locks may be tested or cleared using $result = \text{TEST_LOCK}(lock)$ and CLEAR_LOCK(lock) respectively.

A critical section protects access to a section of code rather than to a data object. The CRITICAL directive marks the beginning of a code region in which only one task can enter at a time. The END CRITICAL directive marks the end of the critical section. Transfer of control into or out of a critical section is prohibited.

```
!HPF$ CRITICAL
      GLOBAL_SUM = GLOBAL_SUM + LOCAL_SUM
!HPF$ END CRITICAL
```

Events are typically used to record the state of a program's execution and to communicate that state to another task. Because they do not set locks, as do the lock routines described earlier, they cannot easily be used to enforce serial access of data. They are suited to work such as signalling other tasks when a certain value has been located in a search procedure. There are four routines needed to perform the event functions, and each requires a mapped argument.

The SET_EVENT(event) routine sets or posts an event; it declares that an action has been accomplished or a certain point in the program has been reached. A task can post an event at any time, whether the state of the event is cleared or already posted. The CLEAR_EVENT(event) routine clears an event, the WAIT_EVENT(event) routine waits until a particular event is posted, and the $result = TEST_EVENT(event)$ function returns a logical value indicating whether a particular event has been posted.

F.5.8 Barrier Removal

You can explicitly remove an implicit barrier after any INDEPENDENT loop, or after any array syntax statement that modifies explicitly mapped arrays, by using the NO BARRIER directive.

!HPF\$ NO BARRIER

F.5.9 Serial Regions

It is often useful to enter a region where only one task is executing. This is particularly useful for certain types of I/O. To facilitate this, two directives are provided. In addition, one may optionally attach a COPY clause to the END MASTER directive which specifies the private data items whose values should be broadcast to all processors. The syntax of this directive is:

```
!HPF$ MASTER sequential\ region ... !HPF$ END MASTER [, COPY( var_1 [, var_2, ..., var_n ])]
```

where var is SYMMETRIC private data to be copied to the same named private data on other processors.

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If a routine is called within a serial region, the routine executes serially; there is no way to get back to parallel execution within the routine. All explicitly mapped data is accessible from within routines called in a serial region, but a routine called from within a serial region cannot allocate explicitly mapped data or remap data. All processors must participate in the invocation of the serial region. Transfer of control into or out of a serial region is not permitted.

F.5.10 Libraries

The HPF Local Routine Library is available in HPF_CRAFT. The HPF_LOCAL extrinsic environment contains a number of libraries that are useful for local SPMD programming and a number of libraries that allow the user to determine global (rather than local) state information. These library procedures take as input the name of a dummy argument and return information on the corresponding global HPF actual argument. They may only be invoked by an HPF_CRAFT procedure that was directly invoked by global HPF code. They may be called only for private data. The libraries reside in a module called HPF_LOCAL_LIBRARY.

The HPF Library is available to HPF_CRAFT when called with data that is explicitly mapped and all processors are participating in the call. In addition, as in HPF_LOCAL, the entire HPF Library is available for use with private data. Mixing private and explicitly mapped data in calls to the HPF library produces undefined behavior.

F.5.11 Parallel Inquiry Intrinsics

These intrinsic functions are provided as an extension to HPF. They return a logical value that provides information to the programmer about the state of execution in a program.

```
IN_PARALLEL()
IN_INDEPENDENT()
```

F.5.12 Task Identity

MY_PE() may be used to return the local processor number. The physical processors are identified by an integer in the range of 0 to n-1 where n is the value returned by the global HPF_LIBRARY function NUMBER_OF_PROCESSORS. Processor identifiers are returned by ABSTRACT_TO_PHYSICAL, which establishes the one-to-one correspondence between the abstract processors of an HPF processors arrangement and the physical processors. Also, the local library function MY_PROCESSOR returns the identifier of the task executing the call.

F.5.13 Parallelism Specification Directives

These directives allow a user to assert that a routine will only be called from within a parallel region, a serial region, or from within both regions. Without these directives an

implementation might be required to generate two versions of code for each routine, depending upon implementation strategies. The directives simply make the generated code size smaller and remove a test.

```
!HPF$ PARALLEL_ONLY
!HPF$ SERIAL_ONLY
!HPF$ PARALLEL_AND_SERIAL
```

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The default is PARALLEL_ONLY.

F.5.14 The SYMMETRIC Directive

SYMMETRIC variables are private data that are guaranteed to be at the same storage location on every processor. The feature is beneficial to implementations that provide one-way communication functionality. One task can either get or put data into another task's symmetric data location, without involving the other task. There is an implicit barrier synchronization after SYMMETRIC data is allocated.

```
REAL PRIV1(100), PRIV2 ! HPF$ SYMMETRIC PRIV1, PRIV2
```

F.5.15 The RESIDENT Directive

The RESIDENT directive can be specified at the loop level and at the routine level. It is an assertion that the references to particular variables in the routine (or loop) are only references to data that are local to the task making the assertion. In the following loop, all references to arrays A, B, and C are local to the task executing each iteration.

F.5.16 The ATOMIC UPDATE Directive

In HPF_CRAFT, the ATOMIC UPDATE directive tells the compiler that a particular data item or the elements of a particular array for a specified operation must be updated atomically. This can be used within loops or in array syntax and applies to both the elements of an array with an assignment of a permutation and the elements of an array within a loop.

In the following example, all references to R(IX(I)) occur atomically, thus eliminating the possibility that different iterations might try to modify the same element concurrently.

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F.5.17 The GEOMETRY Directive

The GEOMETRY directive is similar to a typedef in C, only it is for data mapping. It allows the user to conveniently change the mappings of many arrays at the same time. It is similar in many ways to the TEMPLATE directive, but since it is bound to no particular extent it is sometimes easier to apply.

```
!HPF$ GEOMETRY geom(d_1 \ [, d_2, ..., d_n])
!HPF$ DISTRIBUTE ( geom ) [::] var_1 \ [, var_2, ..., var_m]
Where d_i indicates one of the allowable distribution formats.
!HPF$ GEOMETRY GBB(BLOCK, CYCLIC)
```

```
REAL A(300,300), B(400,400)
!HPF$ DISTRIBUTE (GBB) :: A, B
! if GBB changes then both A and B change
```

附属書G The FORTRAN 77 Local Library

The HPF standard now describes an EXTRINSIC(LANGUAGE='F77', MODEL='LOCAL') interface, or EXTRINSIC(F77_LOCAL) to use the keyword identification (see Section 11.6 for its description), similar in characteristics to the EXTRINSIC(LANGUAGE='HPF', MODEL='LOCAL') and EXTRINSIC(LANGUAGE='FORTRAN', MODEL='LOCAL') interfaces. This section describes a set of library routines to make it easier to make use of the F77_LOCAL interface when passing distributed array data. These library routines can facilitate, for example, a portable blend of global data parallel code with preexisting FORTRAN 77-based code using explicit message passing calls for interprocessor communication. The FORTRAN 77 Local Library interface described in this section was originally developed as part of Thinking Machines TMHPF and is now supported by Sun Microsystems Inc. For suggestions, requests, or corrections concerning this interface, please contact

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G.1 Introduction

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The basic constraints for the local model (Section 11.1) together with the F77_LOCAL-specific argument passing options (Section 11.6) define the nature of the F77_LOCAL interface: how control is to be transferred from a global HPF procedure to a set of local procedures described by an EXTRINSIC(F77_LOCAL) procedure interface and how data can be passed between these two types of procedures: by reference or by descriptor, and with or without temporary local reordering of data to satisfy FORTRAN 77 provisions for sequential, contiguous storage of array data in Fortran array element order. These alternative methods of argument passing can be obtained by use of the two special-purpose attributes for extrinsic dummy arguments defined for LANGUAGE='F77' routines: LAYOUT('F77_ARRAY') (the default) vs. LAYOUT('HPF_ARRAY'), and PASS_BY('**') (the default) vs. PASS_BY('HPF_HANDLE'). However, to take advantage of the option allowing one to pass global HPF array "handles" to local FORTRAN 77 procedures and then obtain information locally about how the local

portion of a given parallel array is actually distributed requires special inquiry routines comparable to the HPF Local Library of functions. Since this library is not only described as a module, but uses many features such as array-valued functions and optional arguments not available in FORTRAN 77 code, it is recommended that a modified FORTRAN 77 interface to this library be provided in the manner described below. Furthermore, there is the problem of describing local portions of parallel arrays in the FORTRAN 77 code used in each local routine called from a global HPF one. Since assumed-shape syntax may not be used, explicit shape arrays are required. But it is common for global distribution of arbitrary sized arrays to result in local portions of arrays that do not have constant shapes on all processors, and the actual extents in each processor cannot necessarily be predicted in advance. In order to allow programmers to obtain axis extent information at run time from the HPF global caller, a special HPF-callable subgrid inquiry subroutine is provided. A FORTRAN 77 callable version of the same routine is also described below, for flexibility in programming.

G.2 Summary

• One HPF-callable subgrid inquiry subroutine

HPF_SUBGRID_INFO

• A set of FORTRAN 77-callable inquiry subroutines

F77_SUBGRID_INFO

F77_GLOBAL_ALIGNMENT

F77_GLOBAL_DISTRIBUTION

F77_GLOBAL_TEMPLATE

F77_ABSTRACT_TO_PHYSICAL

F77_PHYSICAL_TO_ABSTRACT

F77_LOCAL_TO_GLOBAL

F77_GLOBAL_TO_LOCAL

F77_LOCAL_BLKCNT

F77_LOCAL_LINDEX

F77_LOCAL_UINDEX

F77_GLOBAL_SHAPE

F77_GLOBAL_SIZE

F77_SHAPE

F77_SIZE

F77_MY_PROCESSOR

G.3Global HPF Subgrid Inquiry Routine

The F77_LOCAL library interface includes only one global HPF subroutine, HPF_SUBGRID_INFO, whose implementation should be added as an extension to the standard HPF Library module. Its purpose is to provide per-processor information about the local subgrids of distributed arrays. This information is often critical when passing such arrays to local procedures written in FORTRAN 77, where array argument shapes must be stated explicitly in the local procedure (except in the last dimension; there are "assumed size" but no "assumed shape" arrays), but may be expressed in terms of arguments passed at run time ("adjustable shape arrays"). Thus the subgrid parameters obtained from this subgrid inquiry routine can be passed as arguments to the local routines and used there to describe the extents of the locally visible portions of global HPF arrays, as the example in Section G.5 will demonstrates.

HPF_SUBGRID_INFO (ARRAY, IERR, DIM, LB, UB, STRIDE, LB_EMBED, UB_EMBED, AXIS_MAP)

Description. Gives local information about local subgrid allocation onto each pro-

cessor of a distributed array; callable from a global HPF routine.				
Class. Inquiry subro	outine.			
Arguments.				
ARRAY	is a nonsequential array of any type, size, shape, or mapping. It is an INTENT (IN) argument.			
I ERR	is a scalar integer of default kind. It is an INTENT (OUT) argument. Its return value is zero upon successful return and nonzero otherwise. Errors result if local subgrids cannot be expressed as array sections of ARRAY.			
	If any of the optional arguments LB_EMBED, UB_EMBED, or AXIS_MAP is present, then a nonzero value is also returned if the compiler does not organize the local data in serial memory by sequence associating a larger "embedding" array (see Section G.3.1 below for more explanation).			
DIM (optional)	is a scalar integer of default kind. It is an INTENT (IN) argument. DIM indicates the axis along which return values are desired. If DIM is not present, values are returned for all axes.			
LB (optional)	is an INTENT (OUT), default integer array. If this argument is present, and if the value returned in IERR is zero, the values returned in array LB are the lower bounds in			

global coordinates of each processor's subgrid, along one (if DIM is present) or each dimension of ARRAY.

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UB (optional)

is an INTENT (OUT), default integer array. If this argument is present, and if the value returned in IERR is zero, the values returned in array UB are the upper bounds in global coordinates of each processor's subgrid, along one (if DIM is present) or each dimension of ARRAY.

STRIDE (optional)

is an INTENT (OUT), default integer array. If this argument is present, and if the value returned in IERR is zero, the values returned in array STRIDE are the strides in local memory between elements of each processor's subgrid, along one (if DIM is present) or each dimension of ARRAY.

LB_EMBED (optional)

is an INTENT (OUT), default integer array. If this argument is present, and if the value returned in IERR is zero, the values returned in array LB_EMBED are the lower bounds in global coordinates of the actual global array elements allocated on each processor, possibly a superset of the user-visible subgrid, along one (if DIM is present) or each dimension of ARRAY.

UB_EMBED (optional)

is an INTENT (OUT), default integer array. If this argument is present, and if the value returned in IERR is zero, the values returned in array UB_EMBED are the upper bounds in global coordinates of the actual global array elements allocated on each processor, possibly a superset of the user-visible subgrid, along one (if DIM is present) or each dimension of ARRAY.

AXIS_MAP (optional)

is a rank 2, INTENT (OUT), default integer array. If this argument is present, its shape must be at least [n,r], where n is the number of processors and r is the rank of ARRAY.

If the value returned in IERR is zero, the values returned in AXIS_MAP(i,1:r) represent the numbers of the axes of the subgrid on processor i from fastest varying to slowest varying, and form a permutation of the sequence 1,2,...,r.

For the last six arguments, LB, UB, STRIDE, LB_EMBED, UB_EMBED, and AXIS_MAP, each array has a first axis of extent at least n, where n is the number of processors, and the first n indices of that axis of each array must be distributed (perhaps via an explicit CYCLIC or BLOCK distribution) one index per processor. If a second dimension is needed, it should be a collapsed axis of extent at least equal to the rank of ARRAY.

If HPF_SUBGRID_INFO is called, and the elements of ARRAY that are local to any particular processor are not representable as an array section of the global user array, then a nonzero value is returned for IERR. Otherwise, if any of the optional arguments LB, UB, or STRIDE is present, then the lower bounds, upper bounds, or strides, respectively, that describe the local array sections are returned in terms of one-based, global coordinates.

G.3.1 Subgrid Inquiries Involving Embedding Arrays

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In the common case in which the elements of each local subgrid of the global array argument are distributed across processors, with no overlap, and allocated in local memory like a local FORTRAN 77 array, as a contiguous sequence of elements in Fortran array element order, these three last optional arguments would not be required.

However, some implementations may choose less common layouts in local memory, that involve "embedding" these elements in a larger array section of equal rank that is sequence-associated in serial memory. For example, alignment of axes of arrays in different orders may result in a permuting embedding of the subgrid. Or axes of subgrids map be padded with ghost cells, either for stencil optimizations or to achieve same-size subgrids on all nodes.

In variations such as these, we may still view the subgrid as being "embedded" in a sequence associated array which may be accessible in F77_LOCAL operations, if the permutation of axes, shape of any embedding array, and offsets into that array can be obtained at runtime. The last three arguments of HPF_SUBGRID_INFO are provided to allow programmers to obtain this information when it is appropriate, with the help of the IERR flag to signal when this is not the case.

In this mapping, local memory has been allocated for a larger array section, with coordinates (LB_EMBED: UB_EMBED: STRIDE). The coordinates of the *actual* computational elements are limited to the subset (LB: UB: STRIDE). The sequence association is generalized to an arbitrary mapping of axes. Here, AXIS_MAP numbers the axes from fastest varying to slowest varying. If LB_EMBED, UB_EMBED, or AXIS_MAP is specified in a call to HPF_SUBGRID_INFO but ARRAY does not satisfy the assumptions of this mapping model, then a nonzero value is returned for IERR.

G.4 Local FORTRAN 77 Inquiry Routines

Here the F77-callable inquiry subroutines are described briefly. These provide essentially the same capability as the combination of the HPF intrinsic array inquiry functions such as SHAPE and SIZE, together with the HPF LOCAL LIBRARY inquiry routines. The subroutine F77_SUBGRID_INFO serves as a local counterpart to the globally callable subroutine HPF_SUBGRID_INFO described above. In all of the following:

• ARRAY is a dummy argument passed in from a global HPF caller using the LAYOUT ('HPF_ARRAY') attribute and declared within the FORTRAN 77 local subroutine as a scalar integer variable. It is an INTENT (IN) argument.

- DIM is a scalar integer of default kind. It is an INTENT (IN) argument. This argument specifies a particular axis of the global array associated with ARRAY or, if DIM = -1, inquiry is for all axes.
- An "inquiry result" is an INTENT (OUT) argument. If DIM = -1, it is a rank-one array of size equal to at least the rank of the global array associated with ARRAY, returning information associated with all axes. If DIM is positive, the "inquiry result" is a scalar, returning information only for the axis indicated by DIM.
- The arguments are defined in the same way as for the corresponding HPF or HPF_LOCAL routines unless otherwise noted. See the description of HPF_SUBGRID_INFO above and Section 11.7.1 for full specifications of the similarly-named HPF_LOCAL_LIBRARY procedures.

F77_SUBGRID_INFO (ARRAY, IERR1, IERR2, DIM, LB, UB, STRIDE, LB_EMBED, UB_EMBED, AXIS_MAP)

Description. This is a FORTRAN 77-callable version of the HPF subroutine HPF_SUBGRID_INFO.

Arguments.

IERR1	is a scalar integer of default kind. It	tisan INTENT (OUT)
	argument. Its return value is zero i	if LB, UB, and STRIDE

were determined successfully and nonzero otherwise.

IERR2 is a scalar integer of default kind. It is an INTENT

(OUT) argument. Its return value is zero if LB_EMBED and UB_EMBED were determined successfully and nonzero oth-

erwise.

LB, UB, STRIDE, LB_EMBED, UB_EMBED, AXIS_MAP are "inquiry results" of default integer type. They are the lower and upper bounds and strides of the array sections describing the local data (in terms of global indices), the lower and upper bounds of the embedding arrays (again, in terms of global indices), and the axes of the embedding arrays to which the axes of ARRAY are mapped.

F77_GLOBAL_ALIGNMENT (ALIGNEE, LB, UB, STRIDE, AXIS_MAP, IDENTITY_MAP, DYNAMIC, NCOPIES)

Description. This is a FORTRAN 77-callable version of the HPF_LOCAL subroutine GLOBAL_ALIGNMENT. All but the first are INTENT (OUT) arguments whose return values are as specified by the corresponding HPF routine.

Arguments.

ALIGNEE is a dummy argument passed in from global HPF. It is

an INTENT (IN) argument.

LB, UB, STRIDE, AXIS_MAP are integer arrays of rank one. Their size must be at least equal to the rank of the global HPF array associated with ALIGNEE.

IDENTITY_MAP, DYNAMIC are scalar logicals.

NCOPIES is a scalar integer of default kind.

F77_GLOBAL_DISTRIBUTION (DISTRIBUTEE, AXIS_TYPE, AXIS_INFO, PROCESSORS_RANK, PROCESSORS_SHAPE)

Description. This is a FORTRAN 77-callable version of the HPF_LOCAL subroutine GLOBAL_DISTRIBUTION. All but the first are INTENT (OUT) arguments whose return values are as specified by the corresponding HPF routine.

Arguments.

DISTRIBUTEE is a dummy argument passed in from global HPF. It is

an INTENT (IN) argument.

AXIS_TYPE is a CHARACTER*9 array of rank one. Its size must be at

least equal to the rank of the global HPF array associated

with DISTRIBUTEE.

AXIS_INFO is a default integer array of rank one. Its size must be at

least equal to the rank of the global HPF array associated

 $\ with \ {\tt DISTRIBUTEE}.$

PROCESSORS_RANK is a scalar of default integer type.

PROCESSORS_SHAPE is an integer array of rank one. Its size must be at least

equal to the value returned by PROCESSORS_RANK.

F77_GLOBAL_TEMPLATE (ALIGNEE, TEMPLATE_RANK, LB, UB, AXIS_TYPE, AXIS_INFO, NUMBER_ALIGNED, DYNAMIC)

Description. This is a FORTRAN 77-callable version of the HPF_LOCAL subroutine GLOBAL_TEMPLATE. All but the first are INTENT (OUT) arguments whose return values are as specified by the corresponding HPF routine.

Arguments.

ALIGNEE is a dummy argument passed in from global HPF. It is

an INTENT (IN) $\operatorname{argument}.$

TEMPLATE_RANK is a scalar integer of default kind.

LB, UB, AXIS_INFO are integer arrays of rank one. Their size must be at least

equal to the rank of the align-target to which the global HPF array associated with ALIGNEE is ultimately aligned.

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AXIS_TYPE is a CHARACTER*10 array of rank one. Its size must be at

least equal to the rank of the align-target to which the global HPF array associated with ALIGNEE is ultimately

aligned.

NUMBER_ALIGNED is a scalar integer of default kind.

DYNAMIC is a scalar logical.

F77_ABSTRACT_TO_PHYSICAL(ARRAY, INDEX, PROC)

Description. This is a FORTRAN 77-callable version of the HPF_LOCAL subroutine ABSTRACT_TO_PHYSICAL.

Arguments.

INDEX is a rank-one, INTENT (IN), integer array.

PROC is a scalar, INTENT (OUT), integer.

F77_PHYSICAL_TO_ABSTRACT(ARRAY, PROC, INDEX)

Description. This is a FORTRAN 77-callable version of the HPF_LOCAL subroutine PHYSICAL_TO_ABSTRACT.

Arguments.

PROC is a scalar, INTENT (IN), integer.

INDEX is a rank-one, INTENT (OUT), integer array.

F77_LOCAL_TO_GLOBAL(ARRAY, L_INDEX, G_INDEX)

Description. This is a FORTRAN 77-callable version of the HPF_LOCAL subroutine LOCAL_TO_GLOBAL.

Arguments.

L_INDEX is a rank-one, INTENT (IN), integer array.

G_INDEX is a rank-one, INTENT (OUT), integer array.

F77_GLOBAL_TO_LOCAL(ARRAY, G_INDEX, L_INDEX, LOCAL, NCOPIES, PROCS)

Description. This is a FORTRAN 77-callable version of the HPF_LOCAL subroutine GLOBAL_TO_LOCAL.

Arguments.

G_INDEX is a rank-one, INTENT (IN), integer array.

L_INDEX is a rank-one, INTENT (OUT), integer array.

LOCAL is a scalar, INTENT (OUT), logical.

NCOPIES is a scalar, INTENT (OUT), integer.

PROCS is a rank-one, integer array whose size is at least the

number of processors that hold copies of the identified

element.

F77_LOCAL_BLKCNT(L_BLKCNT, ARRAY, DIM, PROC)

Description. This is a FORTRAN 77-callable version of the HPF_LOCAL function LOCAL_BLKCNT.

Arguments.

L_BLKCNT is an "inquiry result" of type integer.

PROC is a scalar integer of default kind. It must be a valid

processor number or, if PROC = -1, the value returned

by F77_MY_PROCESSOR() is implied.

F77_LOCAL_LINDEX(L_LINDEX, ARRAY, DIM, PROC)

Description. This is a FORTRAN 77-callable version of the HPF_LOCAL function LOCAL_LINDEX.

Arguments.

L_LINDEX is a rank-one, integer array of size equal to at least the

value returned by F77_LOCAL_BLKCNT.

DIM may not be -1.

PROC is a scalar integer of default kind. It must be a valid

processor number or, if PROC = -1, the value returned

by F77_MY_PROCESSOR() is implied.

F77_LOCAL_UINDEX(L_UINDEX, ARRAY, DIM, PROC)

Description. This is a FORTRAN 77-callable version of the HPF_LOCAL function LOCAL_UINDEX.

Arguments.

L_UINDEX is a rank-one, integer array of size equal to at least the

value returned by F77_LOCAL_BLKCNT.

DIM may not be -1.

PROC is a scalar integer of default kind. It must be a valid

processor number or, if PROC = -1, the value returned

by F77_MY_PROCESSOR() is implied.

F77_GLOBAL_SHAPE(SHAPE, ARRAY)

Description. This is a FORTRAN 77-callable version of the HPF_LOCAL function GLOBAL_SHAPE.

Arguments.

SHAPE is a rank-one, integer array of size equal to at least the

rank of the global array associated with ARRAY. Its return

value is the shape of that global array.

F77_GLOBAL_SIZE(SIZE, ARRAY, DIM)

Description. This is a FORTRAN 77-callable version of the HPF_LOCAL function GLOBAL_SIZE.

Arguments.

SIZE is a scalar integer equal to the extent of axis DIM of the

global array associated with ARRAY or, if DIM = -1, the

total number of elements in that global array.

F77_SHAPE(SHAPE, ARRAY)

Description. This is a FORTRAN 77-callable version of the HPF intrinsic SHAPE, as it would behave as called from HPF_LOCAL.

Arguments.

SHAPE is a rank-one, integer array of size equal to at least the

rank of the subgrid associated with ARRAY. Its return

value is the shape of that subgrid.

F77_SIZE(SIZE, ARRAY, DIM)

Description. This is a FORTRAN 77-callable version of the HPF intrinsic SIZE, as it would behave as called from HPF_LOCAL.

Arguments.

SIZE

1 2

3

5

6

9

10 11

12 13

14

15

17 18

19

20

212223

24 25

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is a scalar integer equal to the extent of axis DIM of the subgrid associated with ARRAY or, if DIM = -1, the total number of elements in that subgrid.

F77_MY_PROCESSOR(MY_PROC)

Description. This is a FORTRAN 77-callable version of the HPF_LOCAL function MY_PROCESSOR.

Arguments.

MY_PROC

is a scalar, INTENT (OUT), integer. Its value is the identifying number of the physical processor from which this call is made.

G.5 Programming Example Using HPF_SUBGRID_INFO

G.5.1 HPF Caller

```
PROGRAM EXAMPLE
```

```
! Declare the data array and a verification copy
      INTEGER, PARAMETER :: NX = 100, NY = 100
     REAL, DIMENSION(NX,NY) :: X, Y
!HPF$ DISTRIBUTE(BLOCK, BLOCK) :: X, Y
! The global sum will be computed
! by forming partial sums on the processors
     REAL PARTIAL_SUM(NUMBER_OF_PROCESSORS())
!HPF$ DISTRIBUTE PARTIAL_SUM(BLOCK)
! Local subgrid parameters are declared per processor
! for a rank-two array
      INTEGER, DIMENSION(NUMBER_OF_PROCESSORS(),2) ::
    & LB, UB, NUMBER
!HPF$ DISTRIBUTE(BLOCK,*) :: LB, UB, NUMBER
! Define interfaces
     INTERFACE
        EXTRINSIC(F77_LOCAL) SUBROUTINE LOCAL1
         ( LB1, UB1, LB2, UB2, NX, X )
! Arrays LB1, UB1, LB2, UB2, and X are passed by default
```

```
! as LAYOUT('F77_ARRAY') and PASS_BY('*')
                                                                                   1
                  INTEGER, DIMENSION(:) :: LB1, UB1, LB2, UB2
                  INTEGER NX
                  REAL X(:,:)
          !HPF$ DISTRIBUTE(BLOCK) :: LB1, UB1, LB2, UB2
          !HPF$ DISTRIBUTE(BLOCK, BLOCK) :: X
                  END
                  EXTRINSIC(F77_LOCAL) SUBROUTINE LOCAL2(N,X,R)
          ! Arrays N, X, and R are passed by default
          ! as LAYOUT('F77_ARRAY') and PASS_BY('*')
                                                                                  11
                                                                                  12
                  INTEGER N(:)
                                                                                  13
                  REAL X(:,:), R(:)
                                                                                  14
          !HPF$ DISTRIBUTE N(BLOCK)
                                                                                  15
          !HPF$ DISTRIBUTE X(BLOCK, BLOCK)
                                                                                  16
          !HPF$ DISTRIBUTE R(BLOCK)
                                                                                  17
                  END
                                                                                  18
                END INTERFACE
                                                                                  19
                                                                                  20
                                                                                  21
          ! Determine result using only global HPF
                                                                                  22
                ! Initialize values
                                                                                  23
                FORALL (I=1:NX, J=1:NY) X(I,J) = I + (J-1) * NX
                                                                                  ^{24}
                ! Determine and report global sum
                                                                                  25
                PRINT *, 'GLOBAL HPF RESULT: ',SUM(X)
                                                                                  26
          ! Determine result using local subroutines
                                                                                  27
                                                                                  28
                ! Initialize values ( assume stride = 1 )
                                                                                  29
                CALL HPF SUBGRID INFO( Y, IERR, LB=LB, UB=UB )
                                                                                  30
                IF (IERR.NE.O) STOP 'ERROR!'
                                                                                  31
                CALL LOCAL1 ( LB(:,1), UB(:,1), LB(:,2), UB(:,2), NX, Y )
                                                                                  32
                ! Determine and report global sum
                                                                                  33
                NUMBER = UB - LB + 1
                                                                                  34
                CALL LOCAL2 ( NUMBER(:,1) * NUMBER(:,2) , Y , PARTIAL_SUM )
                                                                                  35
                PRINT *, 'F77_LOCAL RESULT #1 : ',SUM(PARTIAL_SUM)
                                                                                  36
                                                                                  37
                END
                                                                                  38
                                                                                  39
G.5.2
       FORTRAN 77 Callee
                                                                                  41
                SUBROUTINE LOCAL1 (LB1, UB1, LB2, UB2, NX, X)
                                                                                  42
          ! The global actual arguments passed to LB1, UB1, LB2, and UB2
                                                                                  43
          ! have only one element apiece and so can be treated as scalars
                                                                                  44
          ! in the local Fortran 77 procedures
                                                                                  45
                INTEGER LB1, UB1, LB2, UB2
                                                                                  46
          ! NX contains the global extent of the first dimension
                                                                                  48
```

```
! of the global array associated with local array X
      INTEGER NX
! Note that X may have no local elements.
     REAL X ( LB1 : UB1 , LB2 : UB2 )
! Initialize the elements of the array, if any
     DO J = LB2, UB2
       DO I = LB2, UB2
         X(I,J) = I + (J-1) * NX
        END DO
     END DO
     END
     SUBROUTINE LOCAL2(N,X,R)
! Here, the rank of the original array is unimportant
! Only the total number of local elements is needed
      INTEGER N
     REAL X(N), R
! If N is zero, local array X has no elements, but R
! still computes the correct local sum
     R = 0.
     DO I = 1, N
        R = R + X(I)
     END DO
     END
```

G.6 Programming Example Using F77-Callable Inquiry Subroutines

This example performs only the initialization of the above example. It illustrates use of the F77-callable inquiry routines on descriptors passed from HPF, as well as the addressing of uncompressed local subgrid data in terms of "embedding arrays."

G.6.1 HPF Caller

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40 41

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```
PROGRAM EXAMPLE

INTEGER, PARAMETER :: NX = 100, NY = 100

REAL, DIMENSION(NX,NY) :: X

! HPF$ DISTRIBUTE(BLOCK, BLOCK) :: X

! Local subgrid parameters are declared per processor
! for a rank-two array

INTEGER, DIMENSION(NUMBER_OF_PROCESSORS(),2) ::

& LB, UB, LB_EMBED, UB_EMBED
! HPF$ DISTRIBUTE(BLOCK,*) :: LB, UB, LB_EMBED, UB_EMBED
! Define interfaces
```

```
EXTRINSIC(F77_LOCAL) SUBROUTINE LOCAL1(
               & LB1, UB1, LB_EMBED1, UB_EMBED1,
               & LB2, UB2, LB_EMBED2, UB_EMBED2, X, X_DESC )
                  INTEGER, DIMENSION(:) ::
                 LB1, UB1, LB_EMBED1, UB_EMBED1,
                   LB2, UB2, LB_EMBED2, UB_EMBED2
          ! X is passed twice, both times without local reordering.
          ! First, it is passed by reference for accessing array elements.
                  REAL, DIMENSION(:,:), LAYOUT('HPF_ARRAY'),
                                                                                 12
                          PASS_BY('*')
                                                                                 13
          ! It is also passed by descriptor for use in F77 LOCAL
                                                                                 14
          ! LIBRARY subroutines only.
                                                                                 15
                  REAL, DIMENSION(:,:), LAYOUT('HPF_ARRAY'),
                                                                                 16
                          PASS_BY('HPF_HANDLE')
               &
                                                            :: X_DESC
                                                                                 17
          !HPF$ DISTRIBUTE(BLOCK) :: LB1, UB1, LB_EMBED1, UB_EMBED1
                                                                                 18
          !HPF$ DISTRIBUTE(BLOCK) :: LB2, UB2, LB_EMBED2, UB_EMBED2
                                                                                 19
          !HPF$ DISTRIBUTE(BLOCK, BLOCK) :: X
                                                                                 20
                                                                                 21
                  END
                                                                                 22
                END INTERFACE
                                                                                 23
          ! Initialize values
                                                                                 ^{24}
          ! ( Assume stride = 1 and no axis permutation )
                                                                                 25
                CALL HPF_SUBGRID_INFO( X, IERR,
                                                                                 26
               & LB=LB, LB_EMBED=LB_EMBED,
                                                                                 27
                                                                                 28
               & UB=UB, UB_EMBED=UB_EMBED)
                                                                                 29
                IF (IERR.NE.O) STOP 'ERROR!'
                                                                                 30
                CALL LOCAL1 (
                                                                                 31
               & LB(:,1), UB(:,1), LB_EMBED(:,1), UB_EMBED(:,1),
                                                                                 32
               & LB(:,2), UB(:,2), LB_EMBED(:,2), UB_EMBED(:,2), X, X)
                                                                                 33
                END
                                                                                 34
                                                                                 35
G.6.2 FORTRAN 77 Callee
                                                                                 36
                                                                                 37
                SUBROUTINE LOCAL1(
                                                                                 38
               & LB1, UB1, LB EMBED1, UB EMBED1,
                                                                                 39
               & LB2, UB2, LB_EMBED2, UB_EMBED2, X, X_DESC )
                                                                                 41
                INTEGER LB1, UB1, LB_EMBED1, UB_EMBED1
                                                                                 42
                INTEGER LB2, UB2, LB_EMBED2, UB_EMBED2
                                                                                 43
          ! The subgrid has been passed in its 'embedded' form
                                                                                 44
                REAL X ( LB_EMBED1 : UB_EMBED1 , LB_EMBED2 : UB_EMBED2 )
                                                                                 45
          ! Locally X_DESC is declared as an INTEGER
                                                                                 46
                INTEGER X_DESC
                                                                                 48
```

INTERFACE

```
! Get the global extent of the first axis % \left( 1\right) =\left( 1\right) \left( 1
      1
                                                                                                                                                                                                                                     ! This is an HPF_LOCAL type of inquiry routine with an
    2
                                                                                                                                                                                                                                     ! 'F77_' prefix
      4
                                                                                                                                                                                                                                                                                                                              CALL F77_GLOBAL_SIZE(NX,X,1)
    5
                                                                                                                                                                                                                                     ! Otherwise, initialize elements of the array
      6
                                                                                                                                                                                                                                     ! Loop only over actual array elements
    7
                                                                                                                                                                                                                                                                                                                              DO J = LB2, UB2
      8
                                                                                                                                                                                                                                                                                                                                                            DO I = LB2, UB2
    9
                                                                                                                                                                                                                                                                                                                                                                                            X(I,J) = I + (J-1) * NX
10
11
                                                                                                                                                                                                                                                                                                                                                              END DO
12
                                                                                                                                                                                                                                                                                                                              END DO
13
                                                                                                                                                                                                                                                                                                                              END
14
15
```